

RAJGAD DNYANPEETH'S

SHRI CHHATRAPATI SHIVAJIRAJE COLLEGE OF ENGINEERING

Gat. No-237, Pune Banglore Highway, Dhangawadi, Tal-Bhor, Dist-Pune (Maharashtra)

Summary of Workshops/ Training Programs/ Seminar/ Conferences organized in the Institute for professional development for teaching and non-teaching staff

Year 2014-15

Sr.	Title of the program	Dates (from-to)	No. of. Days	No of participants (Teaching Staff)	No of participants (Non-Teaching Staff)
(person)	Two days Faculty Development Program on PCB Design	23/3/2015 to 24/3/2015	02	12	Nii







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SHRI CHHATRAPATI SHIVAJIRAJE COLLEGE OF ENGINEERING

Gat.No-237, Pune Banglore Highway, Dhangawadi, Tal-Bhor, Dist-Pune (Maharashtra)

Two days Faculty Development Program on PCB Design

Shri Chhatrapati Shivajiraje College of Engineering

Approved by AICTE, Govt of Maharashtra and Affiliated to the University of Pune (ID NO PU/PN/Engg/376/2009)

Dr. Bhagyashree s. Patil.
Hon. secretary

Anantrao Thopte Founder President Ex. Education Minister Maharashtra State



Date: 18/03/2015

Department of Electronics & Telecommunication Engineering

To, Mr. Ajay Tiwari, Mr. Pushkar More, Patronix Technology, Pune.

Subject: Invitation for conduction of FDP on 'PCB Design' Respected Sir,

On behalf of Rajgad's SCSCOE College, I am very pleased to have the honor of inviting you to conduct FDP on 'PCB Design'. We will be arranging the workshop schedule per your convenience. We are expecting approximately 15 faculties from Our Institute.

Please let us know if you will require any audio-visual equipment or additional technological support. I look forward to meeting you at our Campus.

Best wishes.

Event Coordinator

Received agt

E&TC Engg. Deptt.

HOD
Head of Department
Dept. of E& TC Engineering
Shri Chh. Shivajiraje College of Engg.
Dhangawadi, Pune-412206

Shri Chhatrapati Shivajiraje College of Engineering

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Dr. Bhagyashree s. Patil:

Hon. secretary

Anantrao Thopte
Founder President
Ex. Education Minister
Maharashtra State



Date: 18/03/2015

Department of Electronics & Telecommunication Engineering

To,

Mr. Pushkar More

Patronix Technology,

Pune.

Subject: Invitation for conduction of FDP on 'PCB Design' Respected Sir,

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Event Coordinator

Accepted

ESTC Engg. Dectt.

HOD Head of Department Dept. of E& TC Engineering Shri Chh. Shivajiraje College of Engg. Dhangawadi, Pune-412206

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Gat No. 237, Pune Bangalore Highway, Dhangawadi, Tal - Bhor, Dist- Pune (Maharashtra)

Department of Electronics & Telecommunication

Date: 16/03/2015

NOTICE

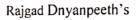
All Teaching Staff are hereby informed that Faculty Development program on "PCB Design" is organized on 23/03/2015 to 24/03/2015 at 11.00 am. All should take note of the same.

Event Coordinator

E&TC Engg. Dept. Comming.

HOD

Head of Department
Dept. of E& TC Engineering
Shri Chh. Shivajiraje College of Engg.
Dhangawadi, Pune-412206





SHRI CHHATRAPATI SHIVAJIRAJE COLLEGE OF ENGINEERING

Gat No. 237, Pune Bangalore Highway, Dhangawadi, Tal – Bhor, Dist- Pune (Maharashtra)

Department of Electronics & Telecommunication

Two days FDP on PCB Design

Date: - 23/3/2015 & 24/3/2015

,	SYLLABUS COVERED DURING WORKSHOP			
DAY 1:	Schedule			
11.00	Sarswati Pujan			
11.00am-11:30pm	Introduction of Expert			
11:30am-1:30pm	Step 1: Design and Output			
	Step 2: From File to Film			
1:30pm -2.00pm	Lunch Break			
	Step 3: Printing the inner layers			
2.00pm -4 pm	Step 4:Removing the unwanted copper			
	Step 5: Layer Allignment			

	SYLLABUS COVERED DURING WORKSHOP			
DAY 2:	Schedule			
	Step 6:Drill			
	Step 7:Plating and Copper Deposition			
11.00am-1:30pm	Step 8:Solder Mask application			
	Step 9:Surface Finish			
	Step 10:Electrical Test			
1:30 pm -2.00pm	Lunch Break			
2.00	Step 11:Surface Finish			
2.00.pm - 4 pm	Step 12:Electrical Test			
4pm - 4:15pm	Vote of Thanks			

Event Coordinator

HOD Head of Department Dept. of E& TC Engineering Shri Chh. Shivajiraje College of Engg. Dhangawadi, Pung-412206



SHRI CHHATRAPATI SHIVAJIRAJE COLLEGE OF ENGINEERING

Gat No. 237, Pune Bangalore Highway, Dhangawadi, Tal - Bhor, Dist- Pune (Maharashtra)

Department of Electronics & Telecommunication

Name of Expert : Mr. Ajay Tiwari and Mr. Pushkar from Patronix Technology, Pune.

Title of Session: Two Days FDP on 'PCB Design'

Date: 23/3/2015 & 24/3/2015

Duration: 2 days

Staff Present: 12

Coordinator: Prof. M. S. Lad

Objective: To get the knowledge about PCB Design and Manufacturing Process.

Description:

'Printed Circuit Board' (PCB) is the most vital element in electronic circuits and equipments. It is also possible to build an electronic circuit with bread boards and zero boards, but the method is a low level and less efficient one wherein the designing circuit is prone to damage and the designing involves a complex process of placing the components of the circuit. However, the invention of the PCB that supports physical electronic components and their wiring through the surface-mounted copper tracks is really remarkable. We can observe at least one PCB in any electronic gadgets ranging from cell phones to computers so arranging Workshop on this topic is found to be very helpful. In this Interactive Workshop Session arranged for Staff, Faculty are able to learn PCB Design Manufacturing Process Step by Step. This is a Practical Workshop in which everyone learns how to design PCB.

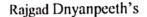
Following content are covered in workshop:

Schematic Design

- ✓ Component Footprint Design
- ✓ Integrated Library Design
- ✓ PCB Design & Routing
- ✓ Etching of PCB
- ✓ Soldering of Components on to PCB

Conclusion:

Faculty Engaged in Interactive workshop and it is found to be very productive and knowledgeable to help students in their miniproject. They were very excited about participation in this workshop and requested for more workshops in similar way so that they can simultaneously gain the practical knowledge.





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Gat No. 237, Pune Bangalore Highway, Dhangawadi, Tal – Bhor, Dist- Pune (Maharashtra)

Department of Electronics & Telecommunication

Photos during Session:







Event Coordinator

E&TC Engg. Deptt.

HOD
Head of Department
Dept. of E& TC Engineering
Shri Chh. Shivajiraje College of Engg.
Dhangawadi, Pune-412206

RAJGAD DNYANPEETH RAJGAD DNYANPEETH TECHNICAL CAMPUS (Shri Chhatrapati Shivajiraje College Of Engineering) S.NO 237, NH-4, Dhangawadi, Tal-Bhor, Dist-Pune

Attendance Sheet for FDP on "Two day PCB workshop"

Day 1: Date 23/3/2015

Sr No	Name of Staff	Signature
1	Prof. Dudhane T.M	(To) Dhave
2	Prof. Dalavi D. U.	Do hi-
3	Prof. Thube M.V.	Mrt-
4	Prof. Gholve M.H.	
5	Prof. Dubal M. B.	MD
6	Prof. Lad M. S.	Macl
7	Prof. Dighe S.S.	Dighe
8	Prof. Sapte M. S.	&-
9	Prof. Velapure A. S.	Avelapura
10	Prof. Sonawane S. M.	Suits
11	Prof. Shinde S. R.	Direct.
12	Prof. J. J. Bandal	The same



RAJGAD DNYANPEETH RAJGAD DNYANPEETH TECHNICAL CAMPUS (Shri Chhatrapati Shivajiraje College Of Engineering) S.NO 237, NH-4, Dhangawadi, Tal-Bhor, Dist-Pune

Attendance Sheet for FDP on "Two day PCB workshop"

Day 1: Date 24/3/2015

Sr No	Name of Staff	Signature
1	Prof. Dudhane T.M	TH Whowe-
2	Prof. Dalavi D. U.	O. hu
3	Prof. Thube M.V.	MUTE
4	Prof. Gholve M.H.	MA
5	Prof. Dubal M. B.	Ms_
6	Prof. Lad M. S.	Weacs
7	Prof. Dighe S.S.	B)191e
8	Prof. Sapte M. S.	
9	Prof. Velapure A. S.	Avelapure
10	Prof. Sonawane S. M.	Sinte_
11	Prof. Shinde S. R.	Grade
12	Prof. J. J. Bandal	in the second se



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Dr. Bhagyashree s. Patil

Anantrao Thopte
Founder President
Ex. Education Minister
Maharashtra State

Date: 24/03/2015

Department of Electronics & Telecommunication

Appreciation Letter

To, Mr. Ajay Tiwari Patronix Technology, Pune

Respected Sir,

We would like to take this opportunity to thank you on behalf of RDTC's SCSCOE, Dhangwadi for taking Two days FDP on 'PCB Design', Conducted from 23/3/2015 to 24/3/2015. It was obvious that you are extremely knowledgeable in your field. It have been seen that everyone participated in workshop captivated by the sessions. We find that the whole area of your expertise in PCB designing is truly fascinating and would ask that you consider a repeat performance at some future date.

Thanking you.

Event Coordinator

Received

HOD

Head of Department

Dept. of E& TC Engineering

Dhangayadi Rune-412206
DTE-8244
SPPU:4071
Dhangaywadi
Pune
412206

Principal

Principal Rajgad Dnyanpeeth's Shri Chhatrapati Shivajiraje College of Engg., Dhangawadi, Pune-412208

Pune Bangalore High-way, Dhangawadi, Tal. Bhor, Dist. - Pune. Pincode - 412206. Telefax. : 02113-282456 / 556 / 656
Email: scscoe@gmail.com \ Website : www.scshivajirajecoe.com \

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Anantrao Thopte

Dr. Bhagyashree s. Patil Hon. secretary

Anantrao Thopte Founder President Ex. Education Minister Maharashtra State



Date: 24/03/2015

Department of Electronics & Telecommunication

Appreciation Letter

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Patronix Technology,

Pune.

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Thanking you.

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SPPU:4071 Dhangawadi

Pune 412206

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Department of Electronics & Telecommunication Feedback Form: Two days FDP on PCB Design

Name of Staff: Poof Gholave M.H.

Sr No.		Excellent	Good	Medium
ı	Depth of			- Industrial
	Knowledge		1	
	Delivered Quality			
	Software and			
	Simulation			
	Level of			
	understanding			ĺ
	Satisfied with			
	Workshop			·

Arrange di more FDP on this type of subject

Staff Signature: (



SHRI CHHATRAPATI SHIVAJIRAJE COLLEGE OF ENGINEERING

Gat No. 237, Pune Bangalore Highway, Dhangawadi, Tal – Bhor, Dist- Pune (Maharashtra)

Feedback Form: Two days FDP on PCB Design

Name of Staff:

Prof J-J. Bandal.

Sr No.		Excellent	Good	Medium
1	Depth of			
	Knowledge			
2	Delivered Quality			
3	Software and			
_	Simulation			
4	Level of			
	understanding			
5	Satisfied with			
	Workshop			

Suggestions:

Staff Signature:

110

About The Program

This course is intended for beginners and those looking for research and development. Syllabus of Program professionally was designed to cover Basic as well as Advance aspects of PCB Designing including following topics

Step 1: Design and Output

Step 2: From File to Film

Step 3: Printing the inner layers

Step 4:Removing the unwanted copper

Step 5: Layer Allignment

Step 6:Drill

Step 7:Plating and Copper Deposition

Step 8:Solder Mask application

Step 9:Surface Finish

Step 10:Electrical Test

Step 11:Surface Finish Step 12:Electrical Test Resource Persons: Ajay Tiwari,

Mr. Pushkar More,

Patronix Technology, Pune.

Rajgad Dnyanpeeth's

SHRI CHHATRAPATI SHIVAJIRAJE COLLEGE OF ENGINEERING

S. No. 237, Satara-Pune, NH-4, Dhangawadi, Tal: Bhor, Dist: Pune (Maharashtra) Two days Faculty Development Program

O

'PCB Design'

(23rd March to 24th March15)

Organized By:

Department of Electronics and Communication Engineering





Important Dates

Last date of Registration: 20th March 2015 Intimation of Confirmation: 21st March 2015 Max. No. of seats: 20

Max. No. of seats: 20 (Registration on first come first serve basis)

Address for Communication

Rajgad Dnyanpeeth Technical Campus Shri Chhatrapati Shivajiraje College Of Engineering. S. No. 237, Satara-Pune, Nh-4, Dhangawadi, Tal: Bhor, Dist: Pune (Maharashtra)

For any query, Contact:

1.Prof. M. S. Sapte

Mob: +91-9456833548

2. Prof.M. S. Lad

Email: mslad@gmail.com Mob: +919430456321