



Rajgad Dnyanpeeth's

**SHRI CHHATRAPATI SHIVAJIRAJE COLLEGE OF ENGINEERING**

Gat No. 237, Pune Bangalore Highway, Dhangawadi, Tal – Bhor, Dist- Pune (Maharashtra)

### Criteria3: Research, Innovations and Extension

#### Key Indicator –3.2 Innovation Ecosystem (10)

3.2.2. Number of workshops/seminars conducted on Intellectual Property Rights (IPR) and Industry-Academia Innovative practices during the last five years (5)

2017-18	2016-17	2015-16	2014-15	2013-14
16	10	9	4	5

Sl. No.	Year	Name of the workshop/ seminar	Date From – To	Date of establishment of IPR cell
1	2017-18	Workshop on Web Development	19/03/2018 To 21/03/2018	28-01-2017
2	2017-18	Seminar on Project finalization and patenting	07/02/2018 To 07/02/2018	28-01-2017
3	2017-18	Workshop on Guidelines of Project Selection	22/01/2018 To 22/01/2018	28-01-2017
4	2017-18	Workshop on CAD & 3D Printing	23/01/2018 To 23/01/2018	28-01-2017
5	2017-18	Workshop on , " Intellectual Property Right"	18/01/2018 To 19/01/2018	28-01-2017
6	2017-18	Workshop on Amazon AWS Cloud	12/01/2018 To 12/01/2018	28-01-2017



7	2017-18	Workshop on LaTeX	27/12/2017 To 28/12/2017	28-01-2017
8	2017-18	Workshop on "Arduino Uno and Raspberry Pi Introduction and Mini Projects"	03/10/2017 To 07/10/2017	28-01-2017
9	2017-18	Workshop on Embedded Systems	11/09/2017 To 12/09/2017	28-01-2017
10	2017-18	Career Opportunities In Computer Engineering	25/08/2017 To 25/08/2017	28-01-2017
11	2017-18	Workshop on How to Publish Research Paper	23/08/2017 To 23/08/2017	28-01-2017
12	2017-18	Workshop on LaTeX	19/08/2017 To 19/08/2017	28-01-2017
13	2017-18	Seminar on Recent Electronic Instruments	19/08/2017 To 19/08/2017	28-01-2017
14	2017-18	Seminar on Arduino Based Automation	11/08/2017 To 11/08/2017	28-01-2017
15	2017-18	Workshop on Power Electronics	04/07/2017 To 04/07/2017	28-01-2017
16	2017-18	Workshop on Programming Language -I (C,C++)	05/06/2017 To 10/06/2017	28-01-2017
17	2016-17	Workshop on Industry Oriented training & Development	10/02/2017 To 10/02/2017	28-01-2017
18	2016-17	Workshop on PLC SCADA Automation	31/01/2017 To 31/01/2017	28-01-2017
19	2016-17	Workshop on Research Methodology	20/01/2017 To 21/01/2017	28-01-2017
20	2016-17	Workshop on Introduction to solid works for Modelling Simulation and analysis	16/01/2017 To 16/01/2017	28-01-2017



21	2016-17	Workshop on ARM Processor and Architecture	11/01/2017 To 11/01/2017	28-01-2017
22	2016-17	Seminar On Embedded System	10/01/2017 To 10/01/2017	28-01-2017
23	2016-17	Workshop on ,"How to Prepare Winning Research Proposal"	14/12/2016 To 15/12/2016	28-01-2017
24	2016-17	Workshop on Career Growth and Opportunities in CCNA	22/07/2016 To 11/09/2016	28-01-2017
25	2016-17	Seminar on Project Guidelines From Idea to Execution	20/07/2016 To 20/07/2016	28-01-2017
26	2016-17	Seminar of Selection of Projects for placement as well as career point of View	17/07/2016 To 17/07/2016	28-01-2017
27	2015-16	Seminar on Fiber Optics & Measurement Equipment Testing	06/01/2016 To 06/01/2016	28-01-2017
28	2015-16	Workshop on QTP,SAP & ORACLE	12/09/2015 To 12/09/2015	28-01-2017
29	2015-16	Workshop on Embedded Systems	10/09/2015 To 10/09/2015	28-01-2017
30	2015-16	Seminar on PLC, SCADA & HMI	09/09/2015 To 09/09/2015	28-01-2017
31	2015-16	Workshop on Career Growth and Opportunities in CCNA	21/08/2015 To 11/10/2015	28-01-2017
32	2015-16	Workshop on Career growth and opportunities in CCNA	21/08/2015 To 26/08/2015	28-01-2017
33	2015-16	Workshop on Raspberry pi	03/08/2015 To 03/08/2015	28-01-2017
34	2015-16	Seminar on Enlightening student through their academic project for placement as well as career point of view	17/07/2015 To 17/07/2015	28-01-2017



35	2015-16	Seminar on Recent Trends in Embedded System & RTOS	02/07/2015 To 02/07/2015	28-01-2017
36	2014-15	Workshop on Red Hat Linux Booting Concept	17/08/2014 To 18/08/2014	28-01-2017
37	2014-15	Seminar on Career Opportunities after BE	11/08/2014 To 11/08/2014	28-01-2017
38	2014-15	Workshop on PIC ARM & RTOS	30/07/2014 To 04/08/2014	28-01-2017
39	2014-15	Seminar on Career Growth & Opportunities in PLC	10/07/2014 To 10/07/2014	28-01-2017
40	2013-14	Seminar on Software Testing	15/03/2014 To 15/03/2014	28-01-2017
41	2013-14	Workshop on Java Programming	21/09/2013 To 21/09/2013	28-01-2017
42	2013-14	Workshop on ARM PROCESSOR	31/08/2013 To 31/08/2013	28-01-2017
43	2013-14	Workshop on Concrete Mix Design	21/08/2013 To 21/08/2013	28-01-2017
44	2013-14	Seminar on Final Year Project selection Guideline	01/07/2013 To 01/07/2013	28-01-2017



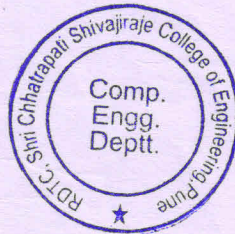
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Report of Workshop On "Web Development"

<u>DEPARTMENT</u>	:	Computer Engineering
<u>RESOURCE PERSON</u>	:	Mr. Laxman Yalmar (Director- Microdynamic Software Pvt. Ltd)
<u>TITLE/TOPIC</u>	:	"Web Development"
<u>DATE</u>	:	19/03/2018 to 21/03/2018
<u>DURATION OF EVENT</u>	:	10.00 AM to 5.00 PM
<u>STUDENT PRESENT</u>	:	46
<u>CO-ORDINATOR</u>	:	Prof. M. R. More
<u>PROGRAM OBJECTIVE</u>	:	<i>"To understand how to train and develop for industry"</i>
<u>CONTENTS</u>	:	<ol style="list-style-type: none"><li>1. HTML / HTML5</li><li>2. CSS / CSS3</li><li>3. BOOTSTRAP</li><li>4. JAVASCRIPT</li><li>5. PHP</li><li>6. ANGULAR JS</li></ol>
<u>PROGRAM OUTCOME</u>	:	<i>"Student will understand what is requirement of company and also how to make carrier in web Development."</i>

Prof. M. R. More  
Co-ordinator



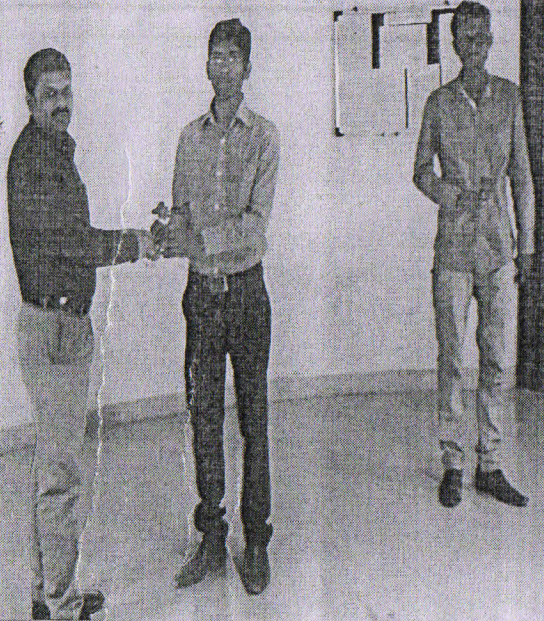
Prof. M. B. Wagh

HOD  
H.O.D

DEPTT. OF COMPUTER ENGG.  
RDTC, Shri Chhatrapati Shivajiraje  
College of Engineering, Dhangwadi,  
Pune- 412206.

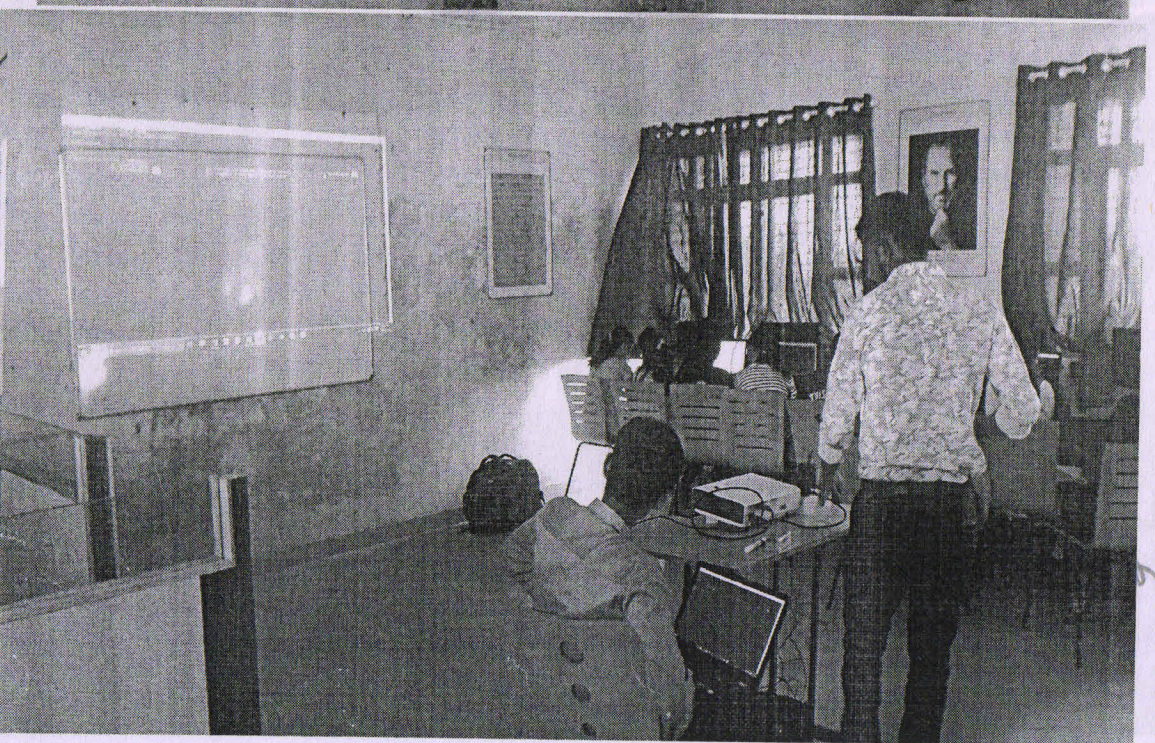
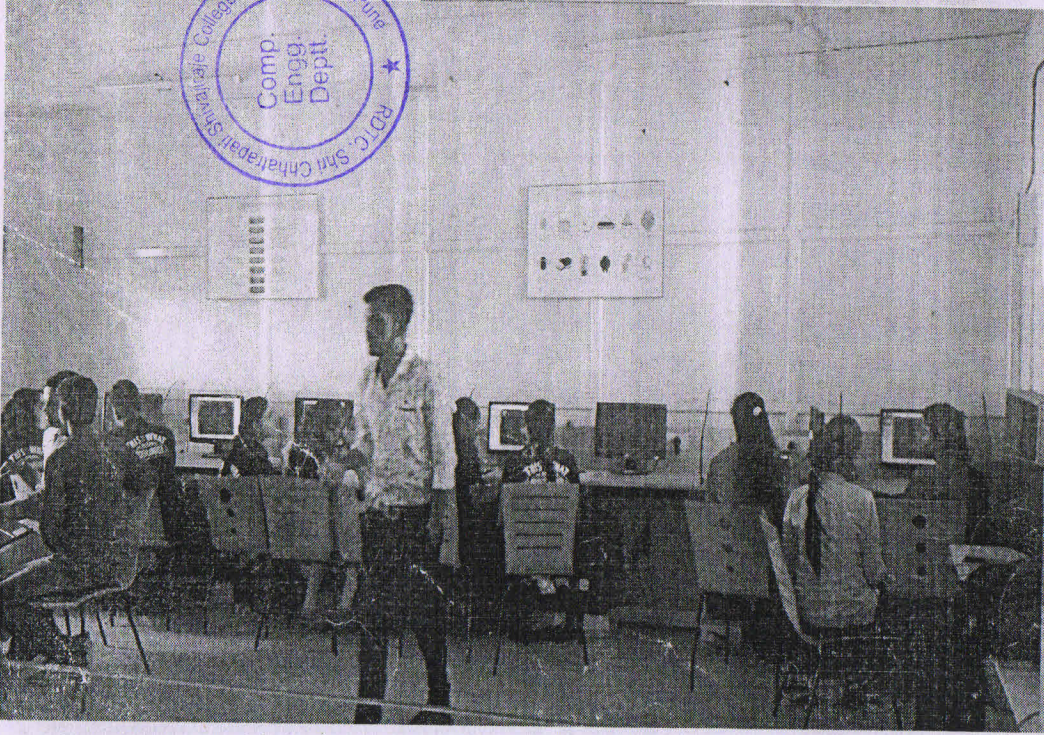
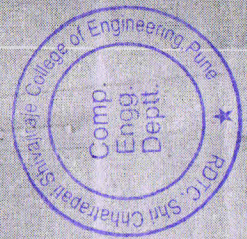
THREE DAYS STATE LEVEL WORKSHOP  
ON  
"WEB DEVELOPMENT"  
(15<sup>TH</sup> TO 17<sup>TH</sup> MARCH, 2018)  
AT

WEL COME  
RAJGAD DNYANPEETH TECHNICAL CAMPUS  
Shri Chhatrapati Shivajiraje College Of Engineering  
Computer Engineering Department  
In association with  
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**REPORT OF SEMINAR ON PROJECT FINALIZATION AND PATENTING****NAME OF GUEST/EXPERT:** Prof. Dr. Suryakant Patil.

Ph.D. Computer Engg., M.E. (CSE), B.E. (CSE), L.L.B (P)

**FROM:** PSP-IP & ASSOCIATES, Intellectual Property Attorney's IP Services & Consultancy, Pune.**TITLE/TOPIC:** Seminar on "Project Finalization and Patenting"**DAY & DATE:** Wednesday-7<sup>th</sup> February 2018**DURATION OF SESSION:** 11.00am to 1.00pm**STUDENT/STAFF PRESENT:** 108/10**CORDINATOR:** Prof. G. S. Jadhav, Dean Research and Development Cell**OBJECTIVE:** To aware & provide the information to the students and staff regarding importance of finalization B.E Project in next year academic year.**DESCRIPTION:** The expert of session Prof. Dr. Suryakant Patil gives brief idea how to finalized project which beneficial to student and that project how to convert into patent.

Also to create awareness about IPR &amp; to look after the activities related to the Intellectual Property, to offer legal services with emphasis on origination, protection and commercialization of the intellectual property, Evaluation and filling of patents, copyrights and design etc consultancy. And also he share all his experience related academic as well as industry to staff and student. Session was very useful for faculty and students for research work and making patent on that work.

**CONCLUSION:** Students are gets idea about project which important for her/him future use.**PHOTO DURING SESSION:**



Photo No.1- Prof. Dr. Suryakant Patil give idea about Finalizing projectfor T.E students

A handwritten signature in black ink, consisting of several overlapping horizontal and diagonal strokes.

Prof. G. S. Jadhav

**Coordinator**

**Dean R & D**

Shri Chh. Shivajiraje College of Engg.  
Dhangawadi, Pune-412206

A handwritten signature in black ink, featuring a stylized 'S' and 'B' followed by a horizontal line.

Prof. Dr. S. B. Patil

**Principal**

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**WORKSHOP REPORT**

DEPARTMENT: E&TC

CLASS: SE and T.E.

NAME OF GUEST/EXPERT: Ms. Rajashri Mahajan

FROM: Dolphin Lab

TITLE/TOPIC: “Centre of Excellence”

DAY & DATE: 22<sup>nd</sup> Jan 2018

DURATION OF SESSION: 1 day

STUDENT/ STAFF PRESENT: 88/2

CO-ORDINATOR: Prof. J. J. Bandal

OBJECTIVE: The main objective is to evoke new project ideas in among student. Also to guide the students about market requirements and the future scope.

DESCRIPTION: Department of Electronics and telecommunication organized Workshop on “Centre of Excellence” on 22<sup>nd</sup> Jan 2018 for SE and TE E&TC students to select their project. Project Selection is a process to assess each project idea and select the project with the highest priority. Projects are still just suggestions at this stage, so the selection is based on only brief descriptions of the project, market requirement and latest technologies.

The final year project is the culmination of the degree – it gives students a chance to demonstrate all they have learned. The project module is very different from other modules. The project tests students' ability to: design, engineer and evaluate quality systems.

The project engineer is also often the primary technical point of contact for the consumer. A project engineer's responsibilities include schedule preparation, pre-planning and resource forecasting for engineering and other technical activities relating to the project.

Project managers in engineering are responsible for leading and directing specialized engineers in specific projects. Strong communication skills and the ability to think quickly to resolve problems in real time are essential in this position, and organizational skills and close attention to detail are also important.



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There are various project selection methods practiced by the organizations. These methods have different features and characteristics. Therefore, each selection method should be best for different organizations. Although there are many differences between these project selection methods, usually the underlying concepts and principles are the same.

**CONCLUSION:** It is always best to have a good plan from the inception, with a list of criteria to be considered and goals to be achieved. The workshop will guide students about the latest technology, market demand and new ideas, use of latest software will also ensure that students do make the right choice .

### PHOTO'S DURING SESSION:



Ms. Rajashree Mahajan guides the students



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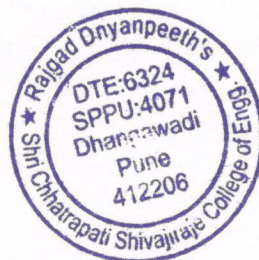


Students during Workshop

Prof J. J. Bandal  
Coordianator

Prof. T.M. Dudhane  
HOD

**Head of Department**  
Dept. of E& TC Engineering  
Shri Chh. Shivajiraje College of Engg.  
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**WORKSHOP REPORT**

DEPARTMENT: E&TC

CLASS: T.E.

NAME OF GUEST/EXPERT: Mr. Yogeshwar Durgude

FROM: Excelling 3DP Pvt Ltd

TITLE/TOPIC: CAD and 3D Printing

DAY & DATE: Wed. 23<sup>rd</sup> Jan 2018

DURATION OF SESSION: 1 day

STUDENT/ STAFF PRESENT: 50/2

CO-ORDINATOR: Prof. J. J. Bandal

OBJECTIVE: The main objective of the program was to impart in depth knowledge of application of Computer Aided Design and 3D Printing. The knowledge gained by the students from this workshop can be extremely helpful for their completion of project.

DESCRIPTION: Department of Electronics and telecommunication organized Workshop on “CAD and 3D Printing on 23<sup>rd</sup> Jan 2018” for SE and TE E&TC students to enhance their knowledge in industry. 3D printers allow you to create prototypes, models and products out of materials such as plastics and metals. The printers do this by creating layer upon layer of your design in your chosen material until the final product is formed. 3D printing is one of the most important technological advancement in additive manufacturing which has been implemented and recognized as a part of modern industry as it has many advantages over conventional approach of which one of the most important factor which is time. This kind of storage costs money but an advantage of 3D printing is that it can help businesses to cut their costs by reducing the amount of storage space they need. Traditional methods are time consuming which meant that it was cost efficient to produce goods in high volumes for storage.

CONCLUSION: After completion of this workshop students are able to understand the 3D printing or additive manufacturing is a process of making three dimensional solid objects from a digital file. The creation of a 3D printed object is achieved using additive processes. ... 3D printing enables you to produce complex (functional) shapes using less material than traditional manufacturing methods.



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PHOTO'S DURING SESSION:



Mr. Durgude explaining 3D Printing



Students during seminar

Prof J. J. Bandal

Coordianator



Prof. T.M.Dudhane

**Head of Department**  
**Head of Department**

Dept. of E& TC Engineering  
Shri Chh. Shivajiraje College of Engg.  
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**REPORT OF TWO DAY WORKSHOP ON INTELLECTUAL PROPERTY RIGHTS****NAME OF GUEST/EXPERT:** Prof. Dr. Suryakant Patil.

Ph.D. Computer Engg, M.E. (CSE), B.E. (CSE), L.L.B (P)

**FROM:** PSP-IP & ASSOCIATES, Intellectual Property Attorney's IP Services & Consultancy, Pune.**TITLE/TOPIC:** Two Day Workshop on "Intellectual Property Rights"**DAY & DATE:** Thursday & Friday-18<sup>th</sup> & 19<sup>th</sup> January 2018**DURATION OF SESSION:** 10am to 5pm**STUDENT/STAFF PRESENT:** 100/33**CORDINATOR:** Prof. G. S. Jadhav, Dean Research and Development Cell**OBJECTIVE:** To aware & provide the information to the students and staff regarding importance of Intellectual Property Rights & to look after the activities related to the Intellectual Property to offer legal services with emphasis on origination, protection and commercialization of the intellectual property, Evaluation and filling of patents, copyrights and design etc consultancy.**DESCRIPTION:** The expert of session Prof. Dr. Suryakant Patil gives brief idea Intellectual Property Right. Intellectual Property Rights (IPR) i.e. Patents, Designs, Copyrights, Trademarks & Geographical Indications etc. Such IPRs are considered as a barometer of R&D activities, encourages technology transfer, national / foreign direct investments & helps in technological / industrial development. Following are course content:

- Intellectual property & trademarks
- Patents & industrial design
- Copyrights & trade secrets (confidential information)
- Geographical indications

Also to create awareness about IPR & to look after the activities related to the Intellectual Property, to offer legal services with emphasis on origination, protection and commercialization of the intellectual property, Evaluation and filling of patents, copyrights and design etc consultancy. And also he share all his experience related academic as well as industry to staff. Session was very useful for faculty and students for research work and making patent on that work.


**CONCLUSION:** Student and Staff are gets aware about the importance of Intellectual Property Rights.**PHOTO DURING SESSION:**



Photo No.1: Dr. Prof. S. B. Patil Sir welcome to Chef Guset Prof. Dr.Suryakant Patil




Photo No.2- Prof. Dr. Suryaakant Patil give idea about IPR to Faculty and students

  
 Prof. G. S. Jadhav  
 Coordinator

**Dean R & D**  
 Shri Chh. Shivajiraje College of Engg.  
 Dhangawadi, Pune-412206



  
 Prof. Dr. S. B. Patil  
**Principal**  
 Rajgad Dnyanpeeth's  
 Chhatrapati Shivajiraje College of Engg.,  
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**REPORT ON ONE DAY WORKSHOP ON “AMAZON AWS CLOUD”**

<b><u>DEPARTMENT</u></b>	:	COMPUTER ENGINEERING
<b><u>RESOURCE PERSON</u></b>	:	Mr. Mandar Bawadekar (Associate Trainer, GNS Technology Pvt. Ltd. Pune)
<b><u>TITLE/TOPIC</u></b>	:	“AMAZON AWS CLOUD”
<b><u>DAY &amp; DATE</u></b>	:	Friday 12/01/2018
<b><u>DURATION OF SESSION</u></b>	:	One Day
<b><u>STUDENTS PRESENT</u></b>	:	TE : 25 BE:22
<b><u>COORDINATOR</u></b>	:	Prof. R. B. Nangare (T & P Coordinator Computer Engg. Department)
<b><u>OBJECTIVE</u></b>	:	To develop various services on Amazon and AWS Cloud Services
<b><u>DESCRIPTION</u></b>	:	<b>Amazon Web Services (AWS)</b> is a comprehensive, evolving <b>cloud computing</b> platform provided by <b>Amazon</b> .  It provides a mix of infrastructure as a service (IaaS), platform as a service (PaaS) and packaged software as a service (SaaS) offerings.
<b><u>CONCLUSION</u></b>	:	GNS Technology Pvt. Ltd. conducted one day workshop on AWS Cloud Platform.





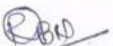
**PHOTO DURING SESSION:**




Mr. Mandar giving knowledge to students



Question Answer Session between Students and Mr. Mandar

  
Prof. R. B. Nangre  
Coordinator



  
Prof. M. B. Wagh  
**Head of Department**  
Dept. of Computer Engineering  
Shri Chh. Shivajiraje College of Engg.  
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**REPORT OF TWO DAY STATE LEVEL WORKSHOP ON LATEX**

NAME OF GUEST/EXPERT: Prof. O. S. Rajankar, Ms. N. M. Gawade

Prof. P. P. Dhaygude, Prof. A. S. Gaware

TITLE/TOPIC: “Two Day State Level Workshop on LaTeX.”

DAY & DATE: Wed. 27/12/2017 to Thur. 28/12/2017

DURATION OF SESSION: 10.00 am to 5.00 pm

STUDENT/STAFF PRESENT: 110

CORDINATOR: Department of Civil Engineering.

OBJECTIVE: To aware & provide the information to the staff regarding importance of LaTeX which is quite useful & plays key role in report writing.

DESCRIPTION: The expert guides the staff about the role of LaTeX in report writing. Speakers gives details about the application of LaTeX software in various aspects of report writing, paper writing such as IEEE & other journal paper etc. Also share the importance & ease of application in formatting.

CONCLUSION: Staff gets aware about the importance & applicability of LaTeX software in preparation of report/thesis/journal papers.

PHOTO DURING SESSION:



**Photo No. 01- Expertise Delivering Session on Application of Latex software**





**Photo No. 02- Participants practicing Application of Latex software**



  
**Coordinator**

**Prof. G. S. Jadhav**  
**Head of Department**  
Dept. of Civil Engineering  
Shri Chh. Shivajiraje College of Engg.  
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**Principal**

**Prof. Dr. S. B. Patil**  
**Principal**  
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**WORKSHOP REPORT****DEPARTMENT:** E&TC**CLASS:** T.E &B.E**NAME OF GUEST:** Mr. Sujith Sakhare**TITLE/TOPIC:** Arduino Uno and Raspberry-Pi Introduction and Mini Projects.**DATE:** 03<sup>rd</sup> Oct 2017 to 07<sup>th</sup> Oct 2017**DURATION OF SESSION:** 5 days**STUDENT/ STAFF PRESENT:** 65/2.**CO-ORDINATOR:** Prof T. S. Zende**OBJECTIVE:**

Introduction to Arduino and perform simple experiments on Arduino Uno Board namely LED blinking, LCD Display, Blinking LED using ADC (Potentiometer). Introduction to Raspberrian OS.

**DESCRIPTION:** Department of Electronics and telecommunication organized Workshop on “Adriano Uno and Raspberry-Pi Introduction and Mini Projects” from 03<sup>rd</sup> Oct 2017 to 07<sup>th</sup> Oct 2017 in seminar hall. Arduino is an open-source hardware and software company, project and user community that designs and manufactures single-board microcontrollers and microcontroller kits for building digital devices and interactive objects that can sense and control objects in the physical and digital world. Arduino boards are available commercially in preassembled form or as do-it-yourself (DIY) kits.

Arduino board designs use a variety of microprocessors and controllers. The boards are equipped with sets of digital and analog input/output (I/O) pins that may be interfaced to various expansion boards or breadboards and other circuits. The boards feature serial communications interfaces, including Universal Serial Bus (USB) on some models, which are also used for loading programs from personal computers. The microcontrollers are typically programmed using a dialect of features from the programming languages C and C++. In addition to using traditional compiler tool chains, the Arduino project provides an integrated development environment (IDE) based on the Processing language project.



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The Arduino Uno has 14 digital input/output pins (of which 6 can be used as PWM outputs), 6 analog inputs, a 16 MHz crystal oscillator, a USB connection, a power jack, an ICSP header, and a reset button. It contains everything needed to support the microcontroller; simply connect it to a computer with a USB cable or power it with a AC-to-DC adapter or battery to get started.

"Uno" means one in Italian and is named to mark the upcoming release of Arduino 1.0. The Arduino Uno and version 1.0 will be the reference versions of Arduino, moving forward. The Uno is the latest in a series of USB Arduino boards, and the reference model for the Arduino platform. The Arduino open-sourced IDE drivers can be downloaded for free.

### Features of the Arduino UNO:

- Microcontroller: ATmega328
- Operating Voltage: 5V
- Input Voltage (recommended): 7-12V
- Input Voltage (limits): 6-20V
- Digital I/O Pins: 14 (of which 6 provide PWM output)
- Analog Input Pins: 6
- DC Current per I/O Pin: 40 mA
- DC Current for 3.3V Pin: 50 mA
- Flash Memory: 32 KB of which 0.5 KB used by bootloader
- SRAM: 2 KB (ATmega328)
- EEPROM: 1 KB (ATmega328)
- Clock Speed: 16 MHz

Raspberry Pi is a trending platform. It is an alternative for Microprocessors in which many boards namely 3B, 3B+ etc are available. Students were demonstrated with downloading the O.S from the internet, they also successfully performed hands-on training during this session on the most common experiments like LED Blinking, Relay ON/OFF and Temperature sensors.



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### CONCLUSION:

This workshop dealt in great detail about the performance of many mini projects that could be created by the students. Introduction to Arduino Uno 328P was also discussed in brief. The students also received the introduction about specifications of the board. After the workshop students were able to handle the board to perform/develop simple projects like turning ON/OFF the Led, Programming GPIO pins, Display message on LCD etc.

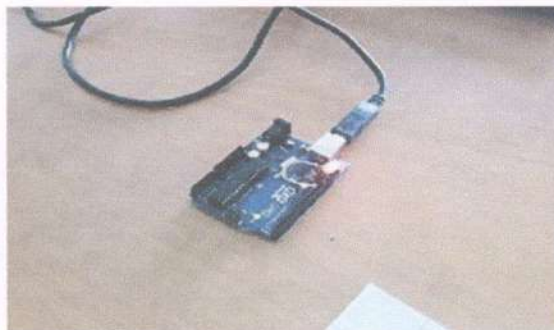
### PHOTOS DURING SESSION :

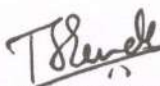


**Mr. Sujith Sakhare interacting with Students during the Session**




**Mr. Sujith Sakhare Guiding Students during the Session**



  
Prof T.S.Zende  
Coordinator



  
Prof. T. M. Dudhane  
HOD E&TC

**Head of Department**  
Dept. of E& TC Engineering  
Shri Chh. Shivajiraje College of Engg.  
Dhangawadi, Pune-412206

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**WORKSHOP REPORT**DEPARTMENT: E&TCCLASS: T.E. & B.E.NAME OF GUEST/EXPERT: Mr. B. S. BhosaleFROM: VCB Electronics, Khed ShivapurTITLE/TOPIC: "Embedded Systems"DAY & DATE: 11<sup>th</sup> Sept 2017 to 12<sup>th</sup> Sept 2017 DURATION OF SESSION: 2 daySTUDENT/ STAFF PRESENT: 77/5CO-ORDINATOR: Prof. J. J. Bandal

OBJECTIVE: The main objective of the program was to impart in depth knowledge of application of Embedded System Design. The knowledge gained by the students from this workshop can be extremely helpful for their project selection and completion on embedded system

DESCRIPTION: Department of Electronics and telecommunication organized Workshop on "Embedded Systems" from 11<sup>th</sup> Sept 2017 to 12<sup>th</sup> Sept 2018 for TE and B.E. E&TC students to enhance their knowledge in industry. 8051 Microcontroller is popular with industrial developers due to their low cost, wide availability and extensive collection of application notes, availability of low cost or free development tools, serial programming & USB capability.

Embedded system design consists of hardware, software, and an environment. Extension of hardware design, nor of software design, must be based on a new foundation that subsumes techniques from both worlds.

This workshop aims at understand Embedded System Design concepts and 8051

Introduction to 8051 architecture

Keil vision3



Rajgad Dnyanpeeth's

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Embedded C Programming

Arithmetic Programming

Timer Programming

Serial port & Interrupt Programming

I/O Programming

### **Hands On with Kit:**

LCD Interfacing

DC Motor control

Data acquisition using sensors

Serial communication interface

Real time clock interfacing

CONCLUSION: Students learned application of embedded system. This workshop helpful to understand Embedded System Design concepts and 8051. The workshop is helpful to select project topic and completion of projects.





Rajgad Dnyanpeeth's

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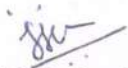
PHOTO'S DURING SESSION:




Mr. Bhosale explaining to students



Students During Seminar

  
Prof. J. J. Bandal  
Co-ordinator



  
Prof. T.M. Dudhane  
**HOD**  
**Head of Department**  
Dept. of E& TC Engineering  
Shri Chh. Shivajiraje College of Engg.  
Dhangawadi, Pune-412206



**REPORT ON ONE DAY SEMINAR ON “CAREER OPPORTUNITIES IN COMPUTER ENGINEERING”**

<b><u>DEPARTMENT</u></b>	:	COMPUTER ENGINEERING
<b><u>RESOURCE PERSON</u></b>	:	Mr. Sharad Kale (Founder President, ieyze technologies Pvt. Ltd. Pune)
<b><u>TITLE/TOPIC</u></b>	:	“Career Opportunities in Computer Engineering”
<b><u>DAY &amp; DATE</u></b>	:	Friday 25/08/2017
<b><u>DURATION OF SESSION</u></b>	:	One Day
<b><u>STUDENTS PRESENT</u></b>	:	TE:13 & BE: 32
<b><u>COORDINATOR</u></b>	:	Prof. R. B. Nangare
<b><u>OBJECTIVE</u></b>	:	To know how many opportunities student have in Various fields after completing Computer Engineering.
<b><u>DESCRIPTION</u></b>	:	

The Department of Computer Engineering organized one day seminar on Career Opportunities in computer for final and third year students to give them knowledge regarding how many opportunities are there in market for computer engineer. Also resource person Mr. Kale gave some guidelines for students how to crack different Government Examination, how to crack aptitude of private organizations.

Also if some student wants to go for fields like SAP, Hardware and networking, Testing then Mr. Kale suggested to join classes and complete certificate courses.

The Resource person gave knowledge about different opportunities and also gives some guidelines to achieve them. He resolves all the queries came from students. Finally vote of thanks done by coordinator Prof. R. B. Nangare.



**PHOTO DURING SESSION:**




Mr. Sharad Kale giving knowledge to students



Question Answer Session with Students

  
Prof. R. B. Nangare  
Coordinator



  
Prof. M. B. Wagh  
HOD  
Head of Department  
Dept. of Computer Engineering  
Shri Chh. Shivajiraje College of Engg.  
Dhangawadi, Pune-412206

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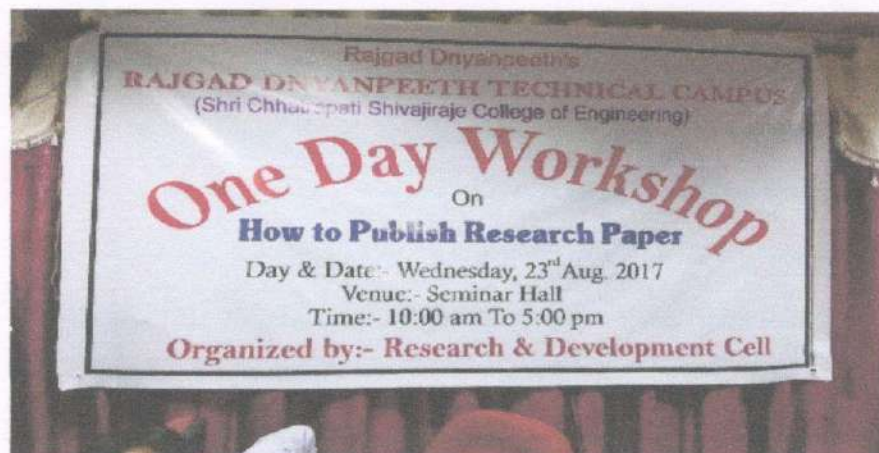

**REPORT OF ONE DAY WORKSHOP ON HOW TO PUBLISH RESEARCH PAPER****NAME OF GUEST/EXPERT:** Mr. Jaykumar Mane**FROM:** International Journal of Innovative Research in Science, Engineering & Technology, Pune.**TITLE/TOPIC:**One Day State Level Workshop on “How to Publish Research Paper.”**DAY & DATE:**Saturday – 23<sup>rd</sup> August 2017**DURATION OF SESSION:**10.30am to 4.30pm**STUDENT/STAFF PRESENT:** 23/38**CORDINATOR:** Prof. G. S. Jadhav, Dean Research and Development Cell**OBJECTIVE:**To aware &provide the information to the staff and students regarding importance of publication of research paper whichis quite useful for the to explore the your innovative work in front of community**DESCRIPTION:**The expert guides the Faculty and students about the role of paper writing and publication in national & international journal Speakertold that One publish a paper to communicate to research community about some new findings/perspectives of a topic. Publishing a paper actually tells that one had solved some original problem and had contributed into the field even if the value added to the field is very incremental.**CONCLUSION:**Facultyand students are getsawareness and importance about publication of paper and getting procedure to write the research paper. And hence lecture was very beneficial to all faculties for doing a research for their future studies.**PHOTO DURING SESSION:**



Photo.No.1 .Mr. JaykumarMane delivers a talk



Photo.No.1 .Mr. JaykumarMane delivers a talk with students and faculty

  
Prof. G. S. Jadhav

**Coordinator**

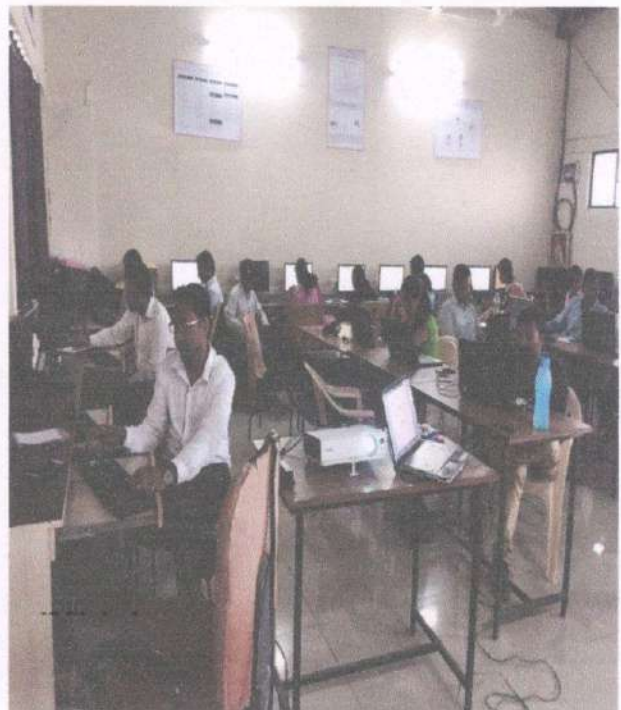
  
Prof. Dr. S. B. Patil

**Principal**  
Rajgad Dnyanpeeth's  
Shri Chhatrapati Shivajiraje College of Engg.,  
Dhangawadi, Pune-412206



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**REPORT OF ONE DAY STATE LEVEL WORKSHOP ON LATEX**NAME OF GUEST/EXPERT: Ms. Gawade Nilam,FROM: Shirur, PuneTITLE/TOPIC: “One Day State Level Workshop on LaTeX.”DAY & DATE: Saturday – 19<sup>th</sup> August 2017DURATION OF SESSION: 10am to 5pmSTUDENT/STAFF PRESENT: 50CORDINATOR: Prof S. R. Sutar.OBJECTIVE: To aware & provide the information to the staff regarding importance of LaTeX which is quite useful & plays key role in report writing.DESCRIPTION: The expert guides the staff about the role of LaTeX in report writing. Speaker gives details about the application of LaTeX software in various aspects of report writing, paper writing such as IEEE & other journal paper etc. Also share the importance & ease of application in formatting.CONCLUSION: Staff gets aware about the importance & applicability of LaTeX software in preparation of report/thesis/journal papers.PHOTO DURING SESSION:

S. R. Sutar

**Workshop Coordinator**

G. S. Adhav

**Head of Department**  
 Dept. of Civil Engineering  
 Shri Chh. Shivajiraje College of Engg,  
 Dhangawadi, Pune-412206

SEMINAR REPORT

DEPARTMENT: E&TC

CLASS: S.E., T.E., BE

NAME OF GUEST/EXPERT: Mr. A. S. Velapure

FROM: PATRONIX TECHNOLOGIES Pvt Ltd

TITLE/TOPIC: “Recent Electronic Instruments”

DAY & DATE: 19<sup>th</sup> August 2017

DURATION OF SESSION: 2 Hours

STUDENT/ STAFF PRESENT: 121/5

CO-ORDINATOR: Prof. J. J. Bandal

OBJECTIVE: The main objective of the program was to impart in depth knowledge and use of recent electronic instruments. The knowledge gained by the students from this seminar can be extremely helpful to test their project.

DESCRIPTION: Department of Electronics and telecommunication organized Workshop on “Recent Electronic Instruments” from 19<sup>th</sup> August 2017 for SE TE and BE E&TC students to enhance their knowledge in instruments. Student knows how to operate digital multi meter, signal generator, cathode ray oscilloscope, supplies etc. Inthis seminar student got knowledge of pulse generator, digital pattern generator and other recent electronic instrument.

**Pulse Generator-** A pulse generator is either an electronic circuit or a piece of electronic test equipment used to generate electrical pulses in different shapes: mostly used for tests at analog or electrical level. Pulse generators are used to control the width, frequency,delay based on the low and high voltage levels of the pulses and with respect to an internal and external triggering. There are three types of pulse generators namely optical pulse generator, bench pulse generators and microwave pulsers.



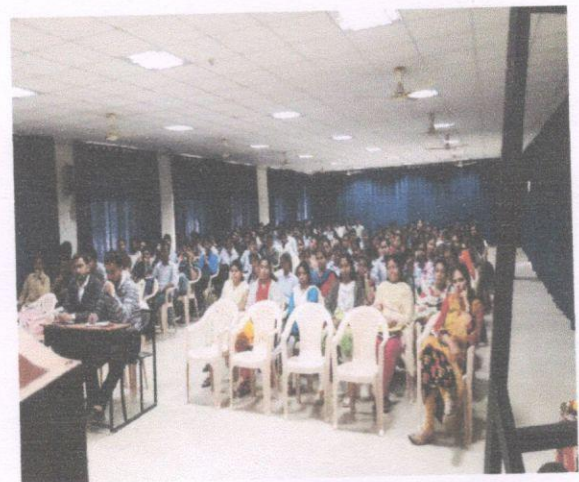
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Digital Pattern Generator-A digital generator is an electronic testing equipment or software used to generate digital electronics stimuli. Digital electronics stimuli are a specific type of electrical waveform varying between two conventional voltages corresponding to two logic gates (either 1 or 0, low or high). The function of the digital pattern generator is to stimulate the inputs of a electronic device. For that purpose, the voltage levels are generated by a digital pattern generator are compared to I/O standards of digital electronics: TTL, LVTTTL and LVDS. It is also known as a logic source because it is a source of synchronous digital stimulus.

CONCLUSION: The seminar will guide students about the latest instruments, its operation and how to test other instruments so that students are able to test their projects.

## PHOTO'S DURING SESSION:



Students during Seminar of Recent Electronic Instrument

Prof J. J. Bandal  
Coordinator

Prof. T. M. Dudhane  
Head of Department

**Head of Department**  
Dept. of E& TC Engineering  
Shri Chh. Shivajiraje College of Engg.  
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**SEMINAR REPORT**

**DEPARTMENT:** E&TC

**CLASS:** TE & BE

**NAME OF GUEST:** Mr. B. S. Bhosale

**TITLE/TOPIC:** Arduino Based Automation

**DATE:** 01/08/2017

**DURATION OF SESSION:** 2 Hrs

**STUDENT/ STAFF PRESENT:** 71/4.

**CO-ORDINATOR:** Prof T. S. Zende

**OBJECTIVE:** The Main objective of this program to get introduce Arduino and understand simple functions of Arduino Uno Board namely LED blinking, LCD Display, Blinking LED using ADC (Potentiometer)

**DESCRIPTION:**

Arduino is an open-source hardware and software company, project and user community that designs and manufactures single-board microcontrollers and microcontroller kits for building digital devices and interactive objects that can sense and control objects in the physical and digital world. Arduino boards are available commercially in pre assembled form or as do-it-yourself (DIY) kits.

Arduino board designs use a variety of microprocessors and controllers. The boards are equipped with sets of digital and analog input/output (I/O) pins that may be interfaced to various expansion boards or breadboards and other circuits. The boards feature serial communications interfaces, including Universal Serial Bus (USB) on some models, which are also used for loading programs from personal computers. The microcontrollers are typically programmed using a dialect of features from the programming languages C and C++. In addition to using traditional compiler tool chains, the Arduino project provides an integrated development environment (IDE) based on the Processing language project.

The Arduino Uno has 14 digital input/output pins (of which 6 can be used as PWM outputs), 6 analog inputs, a 16 MHz crystal oscillator, a USB connection, a power jack, an ICSP header, and a reset button. It contains everything needed to support the microcontroller; simply connect it to a computer with a USB cable or power it with a AC-to-DC adapter or battery to get started. The Arduino Uno differs from all preceding boards in that it does not use the FTDI USB-to-serial driver chip. Instead, it features the Atmega8U2 microcontroller chip programmed as a USB-to-serial converter.

"Uno" means one in Italian and is named to mark the upcoming release of Arduino 1.0. The Arduino Uno and version 1.0 will be the reference versions of Arduino, moving forward. The Uno is the latest in



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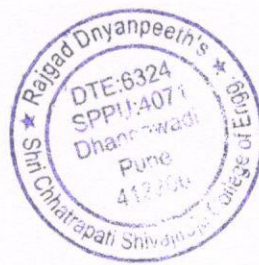
a series of USB Arduino boards, and the reference model for the Arduino platform. The Arduino open-sourced IDE drivers can be downloaded for free.

Features of the Arduino UNO:

- Microcontroller: ATmega328
- Operating Voltage: 5V
- Input Voltage (recommended): 7-12V
- Input Voltage (limits): 6-20V
- Digital I/O Pins: 14 (of which 6 provide PWM output)
- Analog Input Pins: 6
- DC Current per I/O Pin: 40 mA
- DC Current for 3.3V Pin: 50 mA
- Flash Memory: 32 KB of which 0.5 KB used by bootloader
- SRAM: 2 KB (ATmega328)
- EEPROM: 1 KB (ATmega328)
- Clock Speed: 16 MHz

### CONCLUSION:

This seminar dealt in great detail about the performance of many mini projects that could be created by the students. After the workshop students understand the introduction about specifications of the board.

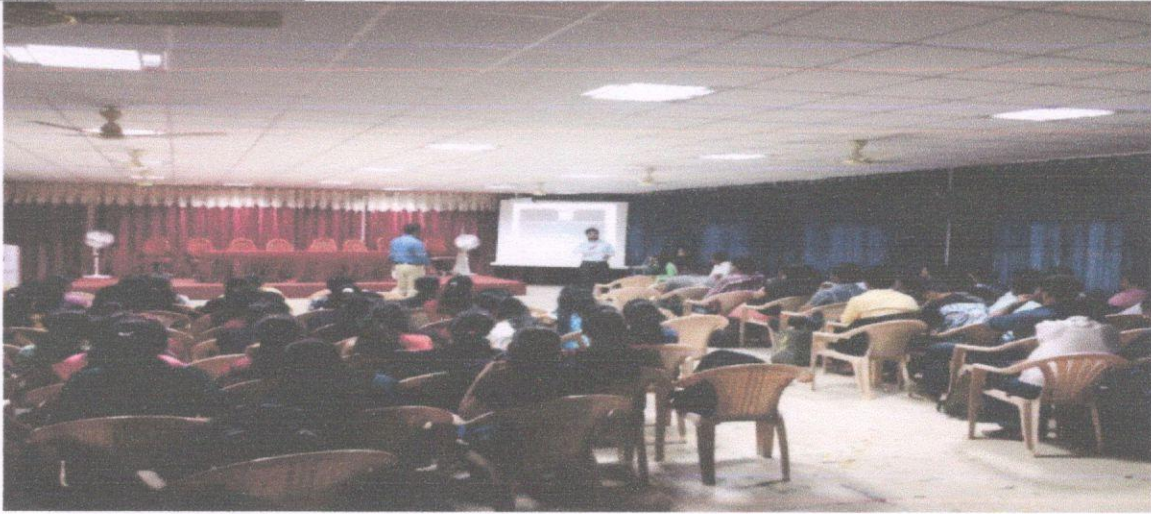


RajgadDnyanpeeth's

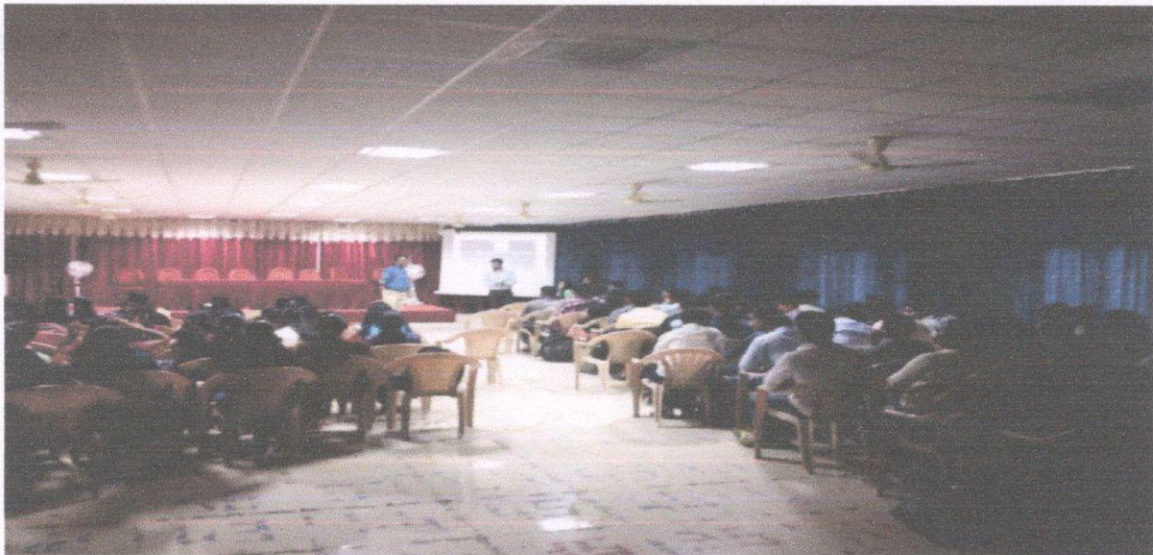
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**PHOTOS DURING SESSION :**



**Mr. Bhosale explaining the Arduino Uno**



**Students during Seminar**

**Prof T. S. Zende  
Coordinator**

**Prof. T. M. Dudhane  
HOD E&TC Dept  
Dept. of E& TC Engineering  
Shri Chh. Shivajiraje College of Engg.  
Dhangawadi, Pune-412206**



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**WORKSHOP REPORT**DEPARTMENT: E&TCCLASS: T.E.NAME OF GUEST/EXPERT: Mr. A. S. VelapureFROM: PATRONIX TECHNOLOGIES Pvt LtdTITLE/TOPIC: "Power Electronics"DAY & DATE: 4<sup>th</sup> July 2017DURATION OF SESSION: 1 daySTUDENT/ STAFF PRESENT: 47/2CO-ORDINATOR: Prof. J. J. Bandal

OBJECTIVE: The main objective of the program was to impart in depth knowledge of application of Embedded System Design. The knowledge gained by the students from this workshop can be extremely helpful for their project selection and completion on embedded system

DESCRIPTION: Department of Electronics and telecommunication organized Workshop on "Power Electronics" from 04<sup>th</sup> July 2017 for TE E&TC students to enhance their knowledge in industry. Power Electronics - Introduction. Power Electronics refers to the process of controlling the flow of current and voltage and converting it to a form that is suitable for user loads. ... It shows the components of a Power Electronic system and how they are interlinked.

**MATLAB** was written originally to provide easy access to matrix software developed by the LINPACK (linear system package) and EISPACK (Eigen system package) projects. **MATLAB** is a high-performance language for technical computing. It integrates computation, visualization, and programming environment.

**Simulink** (Simulation and link) is developed by MathWorks as an add-on with **MATLAB**. It is a graphical programming language which offers modelling, simulation and analyzing of multi domain dynamic systems under Graphical User Interface (GUI) environment.



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For example build a circuit of Bridge Rectifier . "Bridge Rectifier Voltages and Currents" shows how AC voltage is converted to DC Voltage. The dark blue line is the AC voltage on the source side of the bridge. There are two paths for current flow through the diode bridge. The alternating peaks through diodes 1&4 and diodes 2&3 show that current flow reaching the capacitor is flowing in the same direction even though the polarity of the voltage is changing. The ripple in the load voltage corresponds to the charging and discharging of the capacitor.

CONCLUSION: After completion of this workshop students are able to understand the MATLAB is a high-performance language for technical computing. Also therefore students understand the computation, visualization, and programming environment for build ed circuit


### PHOTO'S DURING SESSION:



  
Prof J. J. Bandal

**Workshop Co-ordinator**



  
Prof. T.M. Dudhane

**HOD**  
**Head of Department**  
Dept. of E& TC Engineering  
Shri Chh. Shivajiraje College of Engg.  
Dhangawadi, Pune-412206



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**Department of Computer Engineering**

**Report of Workshop On “Programming Languages - I (C, C++)”**

<u>DEPARTMENT</u>	:	Computer Engineering
<u>RESOURCE PERSON</u>	:	Prof. N. J. Bhojane (Infinity Solutions Pvt. Ltd. Pune)
<u>TITLE/TOPIC</u>	:	“Programming Languages - I (C, C++)”
<u>DATE</u>	:	05/06/2017 to 10/06/2017
<u>DURATION OF EVENT</u>	:	10.00 AM to 5.00 PM
<u>STUDENT PRESENT</u>	:	30
<u>CO-ORDINATOR</u>	:	Prof. P. S. Nagale
<u>PROGRAM OBJECTIVE</u>	:	<i>“To understand concepts of C and C++”</i>
<u>CONTENTS</u>	:	

**A. Introduction of C Language**

1. C Operators
2. Control Structure
3. Loops
4. Arrays
5. Function
6. Pointers

**B. Beginning with C++**

1. Tokens , Expression and Control Structures
2. Function in C++
3. Classes and Objects
4. Operator Overloading
5. Inheritance
6. Pointers, Virtual Functions and Polymorphism
7. Working with Files
8. Templates
9. Exception Handling



PROGRAM OUTCOMES : Students will understand the concept of C language and C++ Programming Language:

Photo During Session :



Prof. P. S. Nagale  
Co-ordinator



Prof. M. B. Wagh  
HOD



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Shri Chhatrapati Shivajiraje College of Engineering  
Dhangwadi, Tal-Bhor, Dist. – Pune – 412206.  
Department of Computer Engineering

**Report of Workshop On "Industry Oriented Training and Development"**

<u>DEPARTMENT</u>	:	Computer Engineering
<u>RESOURCE PERSON</u>	:	Ms. Snehal Biyani (MD, Biyani Technologies, Kolhapur)
<u>TITLE/TOPIC</u>	:	"Industry Oriented Training and Development"
<u>DATE</u>	:	10/02/2017
<u>DURATION OF EVENT</u>	:	10.00 AM to 5.00 PM
<u>STUDENT PRESENT</u>	:	40
<u>CO-ORDINATOR</u>	:	Prof. Shailesh Patil
<u>PROGRAM OBJECTIVE</u>	:	<i>"To understand how to train and develop for industry"</i>
<u>CONTENTS</u>	:	<ol style="list-style-type: none"> <li>1. Preparing for aptitude test</li> <li>2. Preparing for Technical subjects</li> <li>3. Group Discussion</li> <li>4. Facing Interview</li> </ol>

PROGRAM OUTCOME : *"Students will be able to understand how to prepare for aptitude test, technical test, group discussion and interview"*





॥ प्रखलितो ज्ञानमयः प्रदीपः ॥



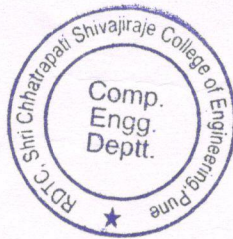
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Department of Computer Engineering



Photo during Session while interacting with Students

*Shailish Patil*

Prof. ShaileshPatil  
Event Coordinator



*Bhagwan P*

Prof. B. D. Thorat  
HOD

**H.O.D**

DEPTT. OF COMPUTER ENGG.  
RDTC, Shri Chhatrapati Shivajiraje  
College of Engineering, Dhangwadi,  
Pune- 412206.

**WORKSHOP REPORT**

DEPARTMENT: E&TC

CLASS: T.E.

NAME OF GUEST/EXPERT: Mr. Choudhari

FROM: Winspire India Training Solution

TITLE/TOPIC: “PLC SCADA Automation”

DAY & DATE: 31<sup>th</sup> Jan 2017

DURATION OF SESSION: 1 day

STUDENT/ STAFF PRESENT: 42/4

CO-ORDINATOR: Prof. D. U. Dalavi

OBJECTIVE: The main objective of the program to develop interest in PLC SCADA. This workshop will cover from basics of definitions to development of projects. The main focus is on learning of PLC technology right from basic fundamental of PLC to complex programming concepts.

DESCRIPTION: Department of Electronics and telecommunication organized Workshop on “PLC SCADA Automation” from 31<sup>th</sup> Jan 2017 for T.E. E&TC students to enhance their knowledge in industry. PLCs are the main heart of the automation system. PLC is the controller used to control the plant/process/system/machine.

SCADA systems are crucial for industrial organizations since they help to maintain efficiency, process data for smarter decisions, and communicate system issues to help mitigate downtime.

The basic SCADA architecture begins with programmable logic controllers (PLCs) or remote terminal units (RTUs). PLCs and RTUs are microcomputers that communicate with an array of objects such as factory machines, HMIs, sensors, and end devices, and then route the information from those objects to computers with SCADA software. The SCADA software processes, distributes, and displays the data, helping operators and other employees analyze the data and make important decisions.



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For example, the SCADA system quickly notifies an operator that a batch of product is showing a high incidence of errors. The operator pauses the operation and views the SCADA system data via an HMI to determine the cause of the issue. The operator reviews the data and discovers that Machine 4 was malfunctioning. The SCADA system's ability to notify the operator of an issue helps him to resolve it and prevent further loss of product.

**CONCLUSION:** PLC is Programmable logic controller. It's hardware, a controller, a device, which can be programmed and placed in the pane. , While SCADA is Supervisory control and data acquisition. It's monitoring software used to monitor and control the PLC, it should be installed in the computer.



Prof. Dalavi introducing to Mr. Choudhari

Prof. D. U. Dalavi  
Workshop Coordinator

Mr. Choudhari explaining students

Prof. T. M. Dudhane  
HOD

**Head of Department**  
Dept. of E& TC Engineering  
Shri Chh. Shivajiraje College of Engg.  
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**Department of Computer Engineering**

**Report of Workshop On “Research Methodology”**

<u>DEPARTMENT</u>	:	Computer Engineering
<u>RESOURCE PERSON</u>	:	Dr. Sandip Chaware (JSPM, Narhe, Pune)
<u>TITLE/TOPIC</u>	:	“Research Methodology”
<u>DATE</u>	:	20 & 21 <sup>st</sup> Jan. 2017
<u>DURATION OF EVENT</u>	:	10.00 AM to 5.00 PM
<u>STUDENT PRESENT</u>	:	40
<u>CO-ORDINATOR</u>	:	Prof. S. M. Jadhav
<u>PROGRAM OBJECTIVE</u>	:	<b><i>“To understand how to create research proposal”.</i></b>
<u>CONTENTS</u>	:	<ol style="list-style-type: none"> <li>1. Writing a good research proposal.</li> <li>2. Approval of research proposal.</li> <li>3. Steps for creating research proposal.</li> </ol>

PROGRAM OUTCOME : ***“Students will be able to understand how to do research and how to create research proposal”.***



॥ प्रखलितो ज्ञानमयः प्रदीपः ॥



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ShriChhatrapatiShivajiraje College of Engineering  
Dhangwadi, Tal-Bhor, Dist. – Pune – 412206.  
Department of Computer Engineering



*S.M. Jadhav*  
Prof. S.M. Jadhav  
(Event Coordinator)



*Bhagwan D*  
Prof. B. D. Thorat  
(Hed of Dept)  
**H.O.D**

DEPTT. OF COMPUTER ENGG.  
RDTC, Shri Chhatrapati Shivajiraje  
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Pune- 412206.



Rajgad Dnyanpeeth's

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**REPORT OF ONEDAY WORKSHOP FOR STUDENT**

**CLASS: - S.E.**

DEPARTMENT: Mechanical Engineering

NAME OF GUEST/EXPERT: Mr. Pranav Yadav

FROM: Pelf Infotech Pvt, Ltd, Kothrud, Pune.

TITLE OF SESSION: One Day Workshop on Introduction to Solidworks for Modeling, Simulation & Analysis.

DAY & DATE: 16<sup>th</sup> Jan 2017 DURATION OF SESSION: 10:30am to 04:45pm

STUDENT PRESENT: 80

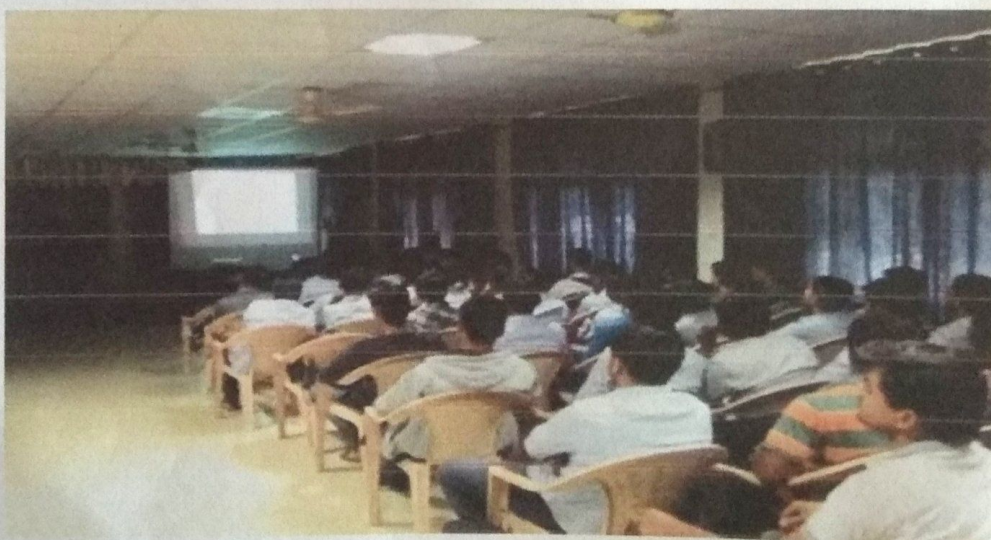
CORDINATOR: Prof. S. V. Ganorkar.

OBJECTIVE: To provide awareness of software for Modeling, Simulation & Analysis.

DISCRIPTION: Mr. Pranav Yadav has explained various application areas of Solidworks software in different Mechanical industries, correlate different modules of Solidworks with different subjects & mechanical applications like structural, thermal, CFD analysis also modeling of different Mechanical & Automobile parts. He also give demonstration of Solidwork software by creating a model, assembly of various parts, analysis of that parts and how to solve the problems during the modeling, assembly and analysis of any assembly.

CONCLUSION: The information is really useful to the students for future prospective.

PHOTO'S DURING SESSION:



*[Signature]*  
**Head of Department**  
Dept. of Mechanical Engineering  
Shri Chh. Shivajiraje College of Engg.  
Dhangawadi, Pune-412206

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**WORKSHOP REPORT**

DEPARTMENT: **E&TC**

CLASS: **B.E.**

NAME OF GUEST/EXPERT: **Mr. Vardhman P. Titwane**

FROM: **V Soft Technology Pune**

TITLE/TOPIC: **“ARM Processor and Architecture”**

DAY & DATE: **11<sup>th</sup> Jan 2017**

DURATION OF SESSION: **1 day**

STUDENT/ STAFF PRESENT: **35/9**

CO-ORDINATOR: **Prof. L. M. Sagale**

OBJECTIVE: The main objective of the program to develop interest in ARM. Motivate students for ARM based students. This workshop will cover the aspects of embedded system design, applications, and current scenario in the industry

DESCRIPTION: Department of Electronics and telecommunication organized Workshop on **“ARM Processor and Architecture” from 10<sup>th</sup> Jan 2017** for B.E. E&TC students to enhance their knowledge in industry. The ARM architecture (Advanced RISC Machine) is a 32-bit RISC processor architecture developed by ARM Limited that is widely used in embedded designs. Because of their power saving features, ARM CPUs are dominant in the mobile electronics market, where low power consumption is a critical design goal. Today, the ARM family accounts for approximately 75% of all embedded 32-bit RISC CPUs, making it one of the most widely used 32-bit architectures. ARM CPUs are found in most corners of consumer electronics, from portable devices (PDAs, mobile phones, media players, handheld gaming units, and calculators) to computer peripherals. ARM processor is optimized for cost and power sensitive MCU and mixed-signal devices for applications such as Internet of Things, connectivity, smart metering, human interface devices, automotive and industrial control systems, domestic household appliances, consumer products and medical instrumentation.



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CONCLUSION: ARM micro controller platforms are very popular today because they provide high performance and power efficiency. ARM processors support both 32 bit and 16 bit computations. A number of architectural features are included to support low power operation. After conduction of this workshop students understand about micro controllers, embedded programming and peripherals for developing optimized embedded systems.







Mr. Vardhaman P. Tiwatane interacting with students

*Sagale*

Asst.Prof.L.M.SAGALE  
Co-ordinator



*Tiwatane*

Prof. T.M.DUDHANE  
HOD (E&TC)  
E & TC Engineering Deptt.  
Shri Chhatrapati Shivaji Maharaj  
College Of Engineering, Dhangawadi,  
Tal-Bhor, Dist-Pune, Pin-412206

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**SEMINAR REPORT**

**DEPARTMENT:** E&TC

**CLASS:** TE & BE

**NAME OF GUEST:** Mr. Sachin Mokashi

**TITLE/TOPIC:** Embedded Systems

**DATE:** 10/01/2017

**DURATION OF SESSION:** 11.00am

**STUDENT/ STAFF PRESENT:** 50/3

**CO-ORDINATOR:** Prof J. J. Bandal

**OBJECTIVE:** The Main objective of this seminar to know the idea and characteristics of Embedded Systems. Also to understand working and applications of Embedded Systems.

**DESCRIPTION:** An *embedded system* is a programmed controlling and operating *system* with a dedicated function within a larger mechanical or electrical *m*, often with real-time computing constraints. It is *embedded* as part of a complete device often including hardware and mechanical parts.

An embedded system works by incorporating a rugged computer board into an industrial enclosure with associated I/O (Input and Output) to fulfil a function in an embedded environment. The computing element is an embedded motherboard, which is the feature packed small form factor, heart of an embedded system.

Embedded systems come in a variety of shapes and sizes, from the largest multiple-rack data storage or networking powerhouses to tiny modules such as your personal MP3 player or cellular handset. Following are some of the usual characteristics of an embedded system:

- Contains a processing engine, such as a general-purpose microprocessor
- Typically designed for a specific application or purpose
- Includes a simple (or no) user interface, such as an automotive engine ignition controller
- Often is resource-limited. For example, it might have a small memory foot-print and no hard drive
- Might have power limitations, such as a requirement to operate from batteries
- Not typically used as a general-purpose computing platform
- Generally has application software built in, not user-selected
- Ships with all intended application hardware and software pre-integrated



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- Often is intended for applications without human intervention

**CONCLUSION:** Embedded System = A computer which is integrated into another system, the embedding system. Embedded systems are part of a bigger system. ... On the whole a hardware chip programmed for a dedicated task results in an embedded system. After this seminar students are able to understand the concept, characteristic and applications of embedded systems.


### PHOTOS DURING SESSION :




Mr. Mokashi explaining the Embedded Systems



Students during the seminar

  
Prof. J. J. Bandal  
Coordinator



  
Prof. T. M. Dudhane  
HOD  
**Head of Department**  
Dept. of E& TC Engineering  
Shri Chh. Shivajiraje College of Engg.  
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**REPORT OF TWO DAY WORKSHOP ON HOW TO PREPARE WINNING RESEARCH PROPOSAL**

**NAME OF GUEST/EXPERT:** Prof.Dr. Chandrashekhar M. Mahajan.  
Ph.D. Physics, M.Sc. (Physics), B.Sc. (Physics)

**FROM:** Vishwakarma Institute of Technology, Pune.

**TITLE/TOPIC:**Two Day Workshop on “How to Prepare Winning Research Proposal”

**DAY & DATE:**Thursday-Wednesday, 14/12/2016-15/12/2016

**DURATION OF SESSION:**Two Days-10.00am-5.00pm

**STAFF PRESENT:**37

**CORDINATOR:**Prof. G. S. Jadhav, Dean Research and Development Cell

**OBJECTIVE:**To aware & provide information to the all staff regarding necessary information about, How to prepare winning research proposal.

**DESCRIPTION:**The expert Prof. Dr. C. M. Mahajan gives brief idea regarding writing of research proposal to all faculties. He was sharing all detail procedure regarding preparation of research proposal and submission of research proposal in their respective fields. The expert guides to staff about the ideology of research and the concept of research methodology in which the different things may arise like what is research? Why is it necessary or need of research etc. The expert gives information about different funding agencies for research e.g. DST, DBT, and MHRD etc.

**CONCLUSION:**This lecture is very beneficial to all faculties for doing a research for their future studies.

**PHOTO DURING SESSION:**

Photo No.1 –Feliction of Chief Guest Prof. Dr. C. M. Mahajan by Principal Prof. Dr. S. B. Patil Sir

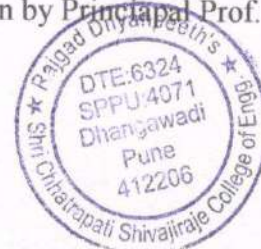





Photo No.2 –Guidance by Prof. Dr. C. M. Mahajan to all faculties

  
Prof. G. S. Jadhav

**Coordinator  
Dean R & D**

Shri Chh. Shivajiraje College of Engg.,  
Dhangawadi, Pune-412206





**Prof. Dr. S. B. Patil  
Principal**

Rajgaon Dnyanpeeth's  
Shri Chhatrapati Shivajiraje College of Engg.,  
Dhangawadi, Pune-412206

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DEPARTMENT: **E&TC**

CLASS: **B.E-2016**

NAME OF GUEST/EXPERT: **Mr. Gajendra Patel**

FROM: **Router mania Pvt. Ltd**

TITLE/TOPIC: **“Career Growth and Opportunities in CCNA”**

DAY & DATE: **Friday -22.7.2016**

DURATION OF SESSION: **30 hrs**

STUDENT/ STAFF PRESENT: **35**

CO-ORDINATOR: **Asst. Prof. D.U.Dalavi.**

CO-COORDINATOR: **Asst. Prof. L.M.Sagale**

OBJECTIVE: Main Objective was to explore the students with what exactly are CCNA and the platform to showcase the ideas for new innovative projects in association with industry & its application.

DESCRIPTION:The Rajgad Dnyanpeeth's Technical Campus, Shree Chhatrapati Shivajiraje College of Engineering, Dhangawadi, Department of Electronics and Telecommunication organized 15 days workshop under Student Welfare Activities from 22/07/2016 to 11/09/2016 in B.E E&TC Class Room which is on “Career Growth and Opportunities in CCNA” for B.E E&TC Students to help them to understand the Industry expectation and Importance of CCNA in Networking Field. For the same the resource person was Mr. Gajendra Patel who has 8 Years Industry Experience and who is Director of Router mania Tech. which is purely Networking Base Company located in Dhankawadi, Pune since year 2006. Along with him Company H.R manager Miss. Anita Rajput and Two Technical experts also present for the workshop. Principal Dr.S.B.Patil issued a notice about the seminar on 18/07/2015. Asst.Prof.D.U.Dalavi is appointed as a Event Coordinator for the same. Asst. Prof. M .S .Sapte. host the event, Prof. T. M. Dudhane who is HOD of the E&TC Department and Asst. Prof. D.U. Dalavi Felicitate the Guest and shares the benefit of such kind of Seminar in Student life. 40 Students from B.E Class attended this seminar.



Mr.Gajendra Patel sir delivered the seminar up to 2 Hours, in that following are the Points he covered.

- General Introduction of CCNA.
- Current Networking Companies Scenario.
- Networking Companies Expectation from the students.
- How to Face Networking Company Interviews.
- Different Levels of CCNA and its Modules.
- At the last Interaction with Student.

**CONCLUSION:** CCNA provides a comprehensive introduction to deploying Router and Switches in computer networks. Students will learn to identify the major components of a computer system, to define basic computer and networking terminology, and to describe the benefits and functions of the OSI reference model. It really gives us an immense pleasure to be with associated with them.

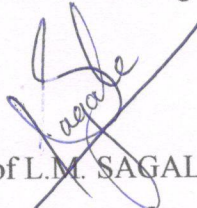
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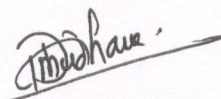
B.E Class During The Seminar



Technical Expert from Routermania Guide the Student

  
Prof L.M. SAGALE  
Event Co-Coordinator

  
Prof D.U. Dalavi  
Event Co-ordinator

  
Prof. T.M.DUDHANE  
HOD



**Head of Department**  
Dept. of E& TC Engineering  
Shri Chh. Shivajiraje College of Engg.  
Dhangawadi, Pune-412206

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**SEMINAR REPORT**

DEPARTMENT: E&TC

CLASS: B.E-2016

NAME OF GUEST/EXPERT: Miss. Pooja And Mr. Jagdish

FROM: Tech Buzz Solutions Pvt. Ltd., Pune

TITLE/TOPIC: "Project Guidelines Idea to Execution"

DAY & DATE: Wednesday - 20.7.2016

DURATION OF SESSION: 2 hrs

STUDENT/ STAFF PRESENT: 30

CO-ORDINATOR: Asst.Prof Mr. L.M.Sagale

OBJECTIVE: Main Objective was to explore the students with what exactly is engineering and the platform to showcase the ideas for new innovative projects in association with industry. Needful information related to campus recruitment and also employment guarantee through academic project.

DESCRIPTION: Projection Selection and its several phases were discussed. Market Trends, different Segments, future Scope, different Technologies, new software; innovative ideas were discussed during the session. On basis of the overwhelming response we wish to carry forward our fruitful professional relation in near future for the benefits of the students a-well for nurturing growth of the institution which is possible only when Industry involvement is there. We wish that such active interaction will be carried futher.

CONCLUSION: The knowledge is really useful to the students while selecting the project. Really it's a proud feeling that such a noble fraternity is providing needful information from the employment point of view for our students. It really gives us an immense pleasure to be with associated with them.





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## PHOTOS DURING SESSION :



Mr. Jagdish During Seminar



Mr. Jagdish Interacting With Students



Mr. Jagdish Guiding Students



Miss. Pooja Guiding Students

Asst. Prof. L.M. SAGALE  
Co-Ordinator



Prof. T.M. DUDHANE  
HOD (E&TC.)  
E & TC Engineering Dept.  
Shri Chhatrapati Shivaji Raja  
College Of Engineering, Dhangawadi,  
Tal-Bhor, Dist-Pune, Pin-412206

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**SEMINAR REPORT**DEPARTMENT: E&TCCLASS: B.E-2016NAME OF GUEST/EXPERT: Mr. Abhishek Kumar and Mr.Govind JadhavFROM: Opulent Infotech, PuneTITLE/TOPIC: "Selection of projects for placement as well as career point of view"DAY & DATE: Friday - 17.7.2016DURATION OF SESSION: 2 hrsSTUDENT/ STAFF PRESENT: 45/03CO-ORDINATOR: Asst.Prof.L.M.Sagale

OBJECTIVE: Main Objective was to explore the students with what exactly is engineering and the platform to showcase the ideas for new innovative projects in association with industry. Needful information related to campus recruitment and also employment guarantee through academic project.

DESCRIPTION: "Opulent Mentorship Program" and its several phases were discussed. They have targeted around 40 colleges for this program in-and around Pune. Overall 100 students will be selected for the placement at Opulent and around 300 students will be placed in their associated partner companies. On basis of the overwhelming response we wish to carry forward our fruitful professional relation in near future for the benefits of the students a well for nurturing growth of the institution which is possible only when Industry involvement is there. We wish that such active interaction will be carried further.

CONCLUSION: Really it's a proud feeling that such a noble fraternity is providing needful information from the employment point of view for our students. It really gives us an immense pleasure to be with associated with them.



PHOTO'S DURING SESSION:



**Parth Doshi Introducing Opulent Infotech**



**Parth Doshi Guiding Student**



**Parth Doshi Interacting With Students**



**Parth Doshi Guiding Students**

**Asst. Prof. L. M. SAGALE**  
**Training & Placement coordinator**



**Prof. T.M.DUDHANE**  
**HOD**

**HOD**  
**E & T C Engineering Dept**  
**Shri Chhatrapati Shivajhaje**  
**College of Engineering**  
**Dhangawadi Tal. Snor**  
**Dist. Pune - 412206**

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**SEMINAR REPORT**

**DEPARTMENT:** E&TC

**CLASS:** BE

**NAME OF GUEST:** Mr. Abhishek Kumar and Mr. Govind Jadhav

**FROM:** Edge World Technologies Pvt. Ltd.

**TITLE/TOPIC:** Fiber Optics and Measurement Equipment Testing

**DATE:** 06/01/2016

**DURATION OF SESSION:** 2Hrs

**STUDENT/ STAFF PRESENT:** 51/02

**CO-ORDINATOR:** Prof L. M. Sagale

**OBJECTIVE:** The Main objective of this seminar to explore the students with Fiber Optic Communication System domain along with industrial job nature and entrepreneurship exposure.

**DESCRIPTION:** Department of Electronics and Telecommunication organized seminar on Fiber Optics and Measurement Equipment Testing for Final Year students to help them to understand importance of fiber optic in communication. The Edge World Technology provides advance and quality test and measurement Telecommunication products to valued customer along with industry ready professional training to technical graduates.

The following points discussed during seminar:

- Telecommunication Technology
- Future Scope based on Employment
- Different test and Measurement Industry
- Digital India- Base Projects
- Smart City Role and Project based on it
- Entrepreneurship Exposure

**CONCLUSION:** Really its proud feeling that such a noble fraternity is providing needful based on Fiber Optics and Measurement Testing from Employment/Entrepreneurship point of view for our students.



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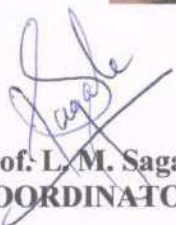
**PHOTOS DURING SEMINAR**




Mr. Kumar and Mr. Jadhav interacting with students



Students during Seminar

  
**Prof. L. M. Sagale**  
**COORDINATOR**



  
**Prof. T. M. Dudhane**  
**Head of Department**  
Dept. of E& TC Engineering  
Shri Chh. Shivajiraje College of Engg.  
Dhangawadi, Pune-412206



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Dhangwadi, Tal-Bhor, Dist. – Pune – 412206.  
**Department of Computer Engineering**

**Report of Workshop On "QTP, SAP and Oracle"**

<u>DEPARTMENT</u>	:	Computer Engineering
<u>RESOURCE PERSON</u>	:	Mr. Arpit Bankadiya (Tech. Lead in Infosys) Mr. Sarath Y. (Tech. Web Consultant in IBM)
<u>TITLE/TOPIC</u>	:	"QTP,SAP and ORACLE"
<u>DATE</u>	:	12/09/2015
<u>DURATION OF EVENT</u>	:	10.00 AM to 5.00 PM
<u>STUDENT PRESENT</u>	:	35
<u>CO-ORDINATOR</u>	:	Prof. D. P. Talele.
<u>PROGRAM OBJECTIVE</u>	:	<b><i>"To understand the detail working of QTP, SAP and Oracle".</i></b>
<u>DESCRIPTION</u>	:	SAP and Oracle are the two market leading ERP packages.  QTP is leading automation testing tool. Students are getting the detail conceptual knowledge about these technologies. Also they get familiar with functional & Technical aspects of ERP and real world problems.
<u>PROGRAM OUTCOME</u>	:	<b><i>"Students will be able to understand how to handle the techniques and get awareness of its use in MNCs".</i></b>



Photos:



*Talele*

Prof. D. P. Talele  
Coordinator



*Bhagwan D*

Prof. B. D. Thorat  
HOD

**Head of Department**  
Dept. of Computer Engineering  
Shri Chh. Shivaji Raju College of Engg.  
Dhule

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**WORKSHOP REPORT**

DEPARTMENT: **E&TC**

CLASS: **T.E. & B.E.**

NAME OF GUEST/EXPERT: **Mr. B. S. Bhosale**

FROM: **VCB Electronics, Khed Shivapur**

TITLE/TOPIC: **“Embedded Systems”**

DAY & DATE: **10<sup>th</sup> Sept 2015**

DURATION OF SESSION: **1 day**

STUDENT/ STAFF PRESENT: **77/5**

CO-ORDINATOR: **Prof. D. U. Dalavi**

OBJECTIVE: The main objective of the program was to impart in depth knowledge of application of Embedded System Design. The knowledge gained by the students from this workshop can be extremely helpful for their project selection and completion on embedded system

DESCRIPTION: Department of Electronics and telecommunication organized Workshop on **“Embedded Systems” on 10<sup>th</sup> Sept 2015** for TE and B.E. E&TC students to enhance their knowledge in industry. Embedded system is the core of every intelligent device. Everything around you from a simple watch to the space ship is using embedded systems. Your mobile phones, PDAs, Washing Machines, Microwave Ovens, Automobile and all the automated electronics gadgets are having one or another form of embedded systems responsible for their intelligent functioning. Electronic devices are rapidly finding applications in many of the areas, from simple coffee vending machines to space science to environmental friendly products such as big belly etc.

The architectural designs of CPU are RISC (Reduced instruction set computing) and CISC (Complex instruction set computing). CISC has the ability to execute addressing modes or





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multi-step operations within one instruction set. It is the design of the CPU where one instruction performs many low-level operations.

A von Neumann architecture has only one bus which is used for both data transfers and instruction fetches, and therefore data transfers and instruction fetches must be scheduled - they can not be performed at the same time. It is possible to have two separate memory systems for a Harvard architecture

CONCLUSION: Students learned application of embedded system. Also they are able to understand the difference between the architectures. The workshop is helpful to select project topic and completion of projects.

### PHOTO'S DURING SESSION:



Mr. Bhosale explaining the features of RISC



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Students during Workshop

Prof. D. U. Dalavi  
Coordianator

Prof. T.M. Dudhane  
Head of Department

**Head of Department**  
Dept. of E& TC Engineering  
Shri Chh. Shivajiraje College of Engg.  
Dhangawadi, Pune-412206



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**SEMINAR REPORT****DEPARTMENT:** E&TC**CLASS:** TE & BE**NAME OF GUEST:** Mr. Sagar Jagtap and Mr. Choudhari**FROM:** Winspire India, Pune**TITLE/TOPIC:** PLC SCADA and HMI**DATE:** 09/09/2015**DURATION OF SESSION:** 2Hrs**STUDENT/ STAFF PRESENT:** 83/5**CO-ORDINATOR:** Prof D. U. Dalavi

**OBJECTIVE:** The Main objective of this seminar to explore the students with exactly is PLC, SCADA and HMI and platform to showcase the ideas for new innovative projects in association with industry and its application.

**DESCRIPTION:** Department of Electronics and Telecommunication organized seminar on PLC SCADA AND HMI on 09/09/2015 for Third Year and Final Year students to help them tounderstand industry expectations and importance of PLC, SCADA and HMI in Automation Field. For the same resource person was Mr. Sagar Jagtap who is marketing manager and Mr. Choudhari who has five years of industry experience. Winspire India is well known company in PLC, SCADA and HMI in Industry.

Mr. Sagar Jagtap and Mr. Choudhari delivered seminar of two hours in the seminar they covered the following points:

- General introduction of PLC, SCADA and HMI
- Current automation companies in market
- Automation companies expectations from students
- Working of PLC and SCADA



- Different modules of **PLC, SCADA & HMI**.
- At the last Interaction with Student.

Event Coordinator Asst. Prof. L.M.Sagale gives the vote of Thanks.

**CONCLUSION:** Really it's a proud feeling that such a noble fraternity is providing the needful information about **PLC, SCADA & HMI** from the student employment point of view. Also it provide the General awareness about **PLC, SCADA & HMI** also the company expectation from the student. It really gives us a immense pleasure to be with associated with them.

PHOTO'S DURING SESSION:



Mr. D. U. Dalavi gives the introduction about seminar.




Mr. Sagar Jagtap H.R Manager guide the student




Mr. Choudhari providing the technical information.



B.E Class during the Seminar

  
**Asst.Prof D.U.Dalavi**  
**Event Coordinator**



  
**Prof. T.M.Dudhane**  
**HOD**  
**E & T C Engineering Dept**  
**Shri Chhatrapati Shivajhaje**  
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**WORKSHOP REPORT**DEPARTMENT: E&TCCLASS: B.E-2015NAME OF GUEST/EXPERT: Mr. Gajendra PatelFROM: Router mania Pvt. LtdTITLE/TOPIC: "Career Growth and Opportunities in CCNA"DAY & DATE: Friday -21.8.2015DURATION OF SESSION: 30 hrsSTUDENT/ STAFF PRESENT: 46CO-ORDINATOR: Prof. D.U.Dalavi.CO-COORDINATOR: Prof. L.M.Sagale

OBJECTIVE: Main Objective was to explore the students with what exactly are CCNA and the platform to showcase the ideas for new innovative projects in association with industry & its application.

DESCRIPTION: The Rajgad Dnyanpeeth's Technical Campus, Shree Chhatrapati Shivajiraje College of Engineering, Dhangawadi, Department of Electronics and Telecommunication organized 15 days workshop under Student Welfare Activities from 21/08/2015 to 04/01/2015 in B.E E&TC Class Room which is on "Carrier Growth and Opportunities in CCNA" for B.E E&TC Students to help them to understand the Industry expectation and Importance of CCNA in Networking Field. For the same the resource person was Mr. Gajendra Patel who has 8 Years Industry Experience and who is Director of Router mania Tech. which is purely Networking Base Company located in Dhankawadi, Pune since year 2006. Along with him Company H.R manager Miss. Anita Rajput and Two Technical experts also present for the workshop. Principal Dr.S.B.Patil issued a notice about the seminar on 18/07/2015. Asst.Prof.D.U.Dalavi is appointed as a Event Coordinator for the same. Asst. Prof. M .S .Sapte. Host the event, Prof. T. M. Dudhane who is HOD of the E&TC Department and Asst. Prof. D.U. Dalavi Felicitate the Guest and shares the benefit of such kind of Seminar in Student life. 46 Students from B.E Class attended this seminar.

Mr.Gajendra Patel sir delivered the seminar up to 2 Hours, in that following are the Points he covered.



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- General Introduction of CCNA.
- Current Networking Companies Scenario.
- Networking Companies Expectation from the students.
- How to Face Networking Company Interviews.
- Different Levels of CCNA and its Modules.
- At the last Interaction with Student.

**CONCLUSION:** CCNA provides a comprehensive introduction to deploying Router and Switches in computer networks. Students will learn to identify the major components of a computer system, to define basic computer and networking terminology, and to describe the benefits and functions of the OSI reference model. It really gives us an immense pleasure to be with associated with them.


### PHOTO'S DURING SESSION:



Prof. T.M.Dudhane Guide the Students



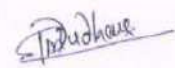
Prof. D.U.Dalavi Felicitate H.R Manager Miss Anita Rajput

  
Prof L.M. SAGALE

Event Co-Coordinator

  
Prof D.U.Dalavi

Event Co-ordinator

  
Prof. T.M.DUDHANE

HOD

**Head of Department**  
Dept. of E& TC Engineering  
Shri Chh. Shivajiraje College of Engg.  
Dhangawadi, Pune-412206





Rajgad Dnyanpeeth's  
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**Department of Computer Engineering**

**Report of Workshop On "Career Growth and Opportunities in CCNA"**

**DEPARTMENT** : Computer Engineering  
**RESOURCE PERSON** : Mr. Gajendra Patel  
 (Routermania Pvt. Ltd.)  
**TITLE/TOPIC** : "Career Growth and Opportunities in CCNA"  
**DATE** : 21/08/2015 to 26/08/2015  
**DURATION OF EVENT** : 10.00 AM to 5.00 PM  
**STUDENT PRESENT** : 22  
**CO-ORDINATOR** : Prof. D. H. Dewarde  
**PROGRAM OBJECTIVE** : Main Objective was to explore the students what exactly is CCNA and the platform to showcase the ideas for new innovative projects in association with industry & its applications.

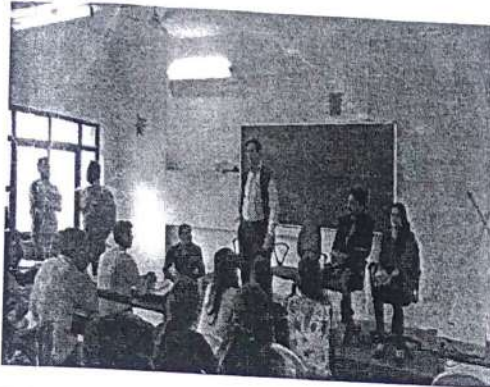
**CONTENTS** :

1. Introduction of CCNA
2. Current Networking Scenarios
3. Networking companies Expectations from students
4. How to face Networking Company Interviews
5. Different Levels of CCNA and its modules
6. Question & Answer

**PROGRAM OUTCOME** : CCNA provides a comprehensive introduction to deploying Router and switches in Computer networks. Students have learnt to identify the major components of a computer system, to define basic computer and networking terminology, and to describe the benefits and functions of the OSI reference model.



PHOTO'S DURING SESSION:



Prof. T.M.Dudhane Guide the Students



Prof. D.U.Dalavi Felicitate H.R Manager Miss Anita Rajput



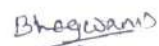
B.E Class During The Seminar



Technical Expert from Routermania Guide the Student

  
Asst.Prof D.H. Dewarde  
Event Co-ordinator



  
Prof. B.D. Thorat

**HOD**  
**H.O.D**  
DEPT. OF COMPUTER ENGG.  
RDTC, Shri Chhatrapati Shivajiraje  
College of Engineering, Dhingwadi,  
Pune - 412206.



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**WORKSHOP REPORT**

**DEPARTMENT:** E&TC

**CLASS:** BE

**NAME OF GUEST:** Mr. Pushkar More

**FROM:** Patronix Technologies Pvt. Ltd.

**TITLE/TOPIC:** Raspberry Pi

**DATE:** 03/08/2015

**DURATION OF SESSION:** 2Hrs

**STUDENT/ STAFF PRESENT:** 75/10

**CO-ORDINATOR:** Prof L. M. Sagale and Prof. A. S. Velapure

**OBJECTIVE:** The Main objective of this workshop to discuss what a Raspberry Pi is, the different models that are available and their qualities. Also this workshop will helpful to learn OS installation on SD card.

**DESCRIPTION:** Department of Electronics and telecommunication organized Workshop on “Raspberry Pi” on 03<sup>rd</sup> Aug 2015 for B.E. E&TC students to enhance their knowledge in internet of things.

The Raspberry Pi is a low cost, credit-card sized computer that plugs into a computer monitor or TV, and uses a standard keyboard and mouse. It is a capable little device that enables people of all ages to explore computing, and to learn how to program in languages like Scratch and Python. The Raspberry Pi is a very cheap computer that runs Linux, but it also provides a set of GPIO (general purpose input/output) pins that allow you to control electronic components for physical computing and explore the Internet of Things (IoT). To install Raspbian, you will need to write the operating system image file onto the SD card. Start by uncompressing the ZIP file you downloaded from raspberrypi.org. The raspberry pi boards are used in many applications like Media streamer, Arcade machine, Tablet computer, Home automation, Internet radio, Controlling robots, Cosmic Computer, Hunting for meteorites, Coffee and also in raspberry pi based projects.

**CONCLUSION:** After the workshop students are able to understand the Raspberry Pi is a powerful little beast and a great platform for building low-cost, but highly capable, embedded systems. Students are able to understand the application of Raspberry pi and job opportunities in Internet of things



PHOTO'S DURING SESSION:



**Pushkar More Guiding Students**



**Pushkar More Interacting with students**





**Pushkar More taking Feedback**



**Staff and Students at Seminar**

  
**Asst. Prof L.M. SAGALE**  
TNP- Coordinator

  
**Asst. Prof A.S.VELAPURE**  
Department of E&TC

  
**Prof. T.M.DUDHANE**  
**HOD**  
**HOD**  
E & T C Engineering Dept  
Shri Chhatrapati Shivajhaje  
College of Engineering  
Dhangawadi Tal. Bhor  
Dist. Pune - 412206





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Dhangwadi, Tal-Bhor, Dist. – Pune – 412206.  
**Department of Computer Engineering**

Date: 17/07/2015

### Seminar Report

On

**“Enlightening Students through their academic projects for placement as well as career point of view”**

As per the guidance of AICTE, DTE and Savitribai Phule University of Pune, The Rajgad Dnyanpeeth's Shree Chhatrapati Shivajiraje College of Engineering Dhangwadi, Department of Computer Engineering organized under **Student Welfare Activity** on 17/17/2015 at 11.30 AM in seminar hall which is on “Enlightening Students through their academic projects for placement as well as career point of view” for the final year students to help them to select their academic projects. For the same the resource person is **Mr. Parth Doshi** and **Ms. Vrushali Khopkar** Active employee of OPULENT InfoTech, Pune has successfully conducted and completed Training & Placement orientation seminar on the same.

Principal Dr. S. B. Patil issued a notice about the seminar on 16/07/2015. Prof. V. N. Vanarote appointed as Event Coordinator for the same. Prof. B. D. Thorat HOD of Computer Department felicitates the guest and share the benefit of such kind of seminar in student life. Around 30 students from final year attended this seminar.

Mr. Parth Doshi sir and Ms. Vrushali Khopkar madam delivered the seminar up to 2 Hours, in that focused objectives.

**OBJECTIVE:** Main objective was to explore the students with what exactly is engineering and the platform to showcase the ideas for new innovative projects in association with industry. Needful information related to campus recruitment and also employment guarantee through academic project.

Event coordinator Prof. V. N. Vanarote gives the vote of thanks.

**CONCLUSION:** Really it's a proud feeling that such a noble fraternity is providing needful information from the employment point of view for our students. It really gives us an immense pleasure to be with associated with them.



**Photos During Session:**



*Vanarote V.N.*

**Prof. V. N. Vanarote  
Coordinator**



*Bhogwand*

**Prof. B. D. Thorat  
HOD**

**Head of Department**  
Dept. of Computer Engineering  
**Shri Chh. Shivaji College of Engg.**  
Dhangawadi, Pune-412206

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**SEMINAR REPORT****DEPARTMENT:** E&TC**CLASS:** TE & BE**NAME OF GUEST:** Mr. Chinmay Oak**FROM:** Excel Technologies, Katraj**TITLE/TOPIC:** Recent Trends in Embedded Systems and RTOS**DATE:** 02/07/2015**DURATION OF SESSION:** 2Hrs**STUDENT/ STAFF PRESENT:** 90/5**CO-ORDINATOR:** Prof D. U. Dalavi

**OBJECTIVE:** The Main objective of this seminar to explore the students with exactly Embedded industry Expectations are and also provide the platform to showcase the ideas for new innovative projects in association with industry and its application..

**DESCRIPTION:** Department of Electronics and Telecommunication organized seminar on Recent Trends in Embedded System and RTOS on 02/07/2015 for Third Year and Final Year students to help them to select their Mini Project and Final Year Project . The resource is Mr. Chinmay Oak who has 5 Years Industrial Experience and who is owner of Excel Technologies which is purely Embedded Base Company located in Katraj since 2004. Principal Sir Dr. S. B. Patil sir issued a notice about the seminar on 30.06.2015. Prof. D. U. Dalavi is appointed as a Event Coordinator for the same. Prof. M. S. Sapte host the event , Prof. T. M. Dudhane who is Head of Department of the E & TC Department Felicitate the Guest and shares the benefit of such kind of Seminar in Student life.

Embedded system is core of every intelligent device . Everything around you from a simple watch to space ship is using Embedded systems. Everything phones, PDA's Washing Machines, Microwave ovens, Automobile and all automated gadgets are having one or another form of embedded system responsible for their intelligent functioning.



Mr. Chinmay Oak delivered the seminar on following Points.

- General Introduction to Embedded System and RTOS
- Embedded Device Development Cycle
- How to execute the BE and TE level Project
- General Expectation of Company from Student Side
- How to Face the Interview

Event Coordinator Prof. D.U.Dalvi gives the vote of Thanks.

**CONCLUSION:** "Recent Trends in Embedded System and RTOS provides a comprehensive introduction about what exactly industry demands from the students. It really gives us an immense pleasure to be with associated with them.

PHOTO'S DURING SESSION:



Prof. M.S.Sapte Hosting the Event.



Prof. T.M.Dudhane Guiding the Students



Mr. Chinmay Oak Conducting the Seminar



Students during the Seminar

  
Asst. Prof. D.U.Dalvi  
EVENT COORDINATOR



  
Prof. T.M.Dudhane

HOD  
HOD  
E & T C Engineering Dept  
Shri Chhatrapati Shivajiraje  
College of Engineering  
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**Department of Computer Engineering**

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Date :21/08/2014

### A Report on RED HAT LINUX Workshop

As per guidelines of AICTE , DTE & Savitribai Phule University of Pune, The Rajgad Dnyanpeeth's Shri Chhatrapati Shivajiraje College of Engineering, Dhangwadi, Department Computer Engineering organized two days workshop under Student Welfare Activities from 17<sup>th</sup> Aug 2014 to 18<sup>th</sup> Aug 2014 @ 10.00 AM in Seminar Hall which is on “ **RED HAT LINUX BOOTING COMCEPTS**” for all second Year, Third Year & Final Year Students of Computer Engineering to help them to get knowledge of booting & utilize the same for final year projects. For the same resource person is Mr. Yogesh Babar who has completed more than five years in industry working in same area. Principal Dr. R. L. Khandagale issued the notice on 16<sup>th</sup> Aug 2014. Prof. B. D. Thorat appointed as Event Coordinator.



*Bhogewar*

Prof. B. D. Thorat

HOD Computer Dept.

**Head of Department**

Dept. of Computer Engineeringg

Shri Chh. Shivajiraje College of Enggg

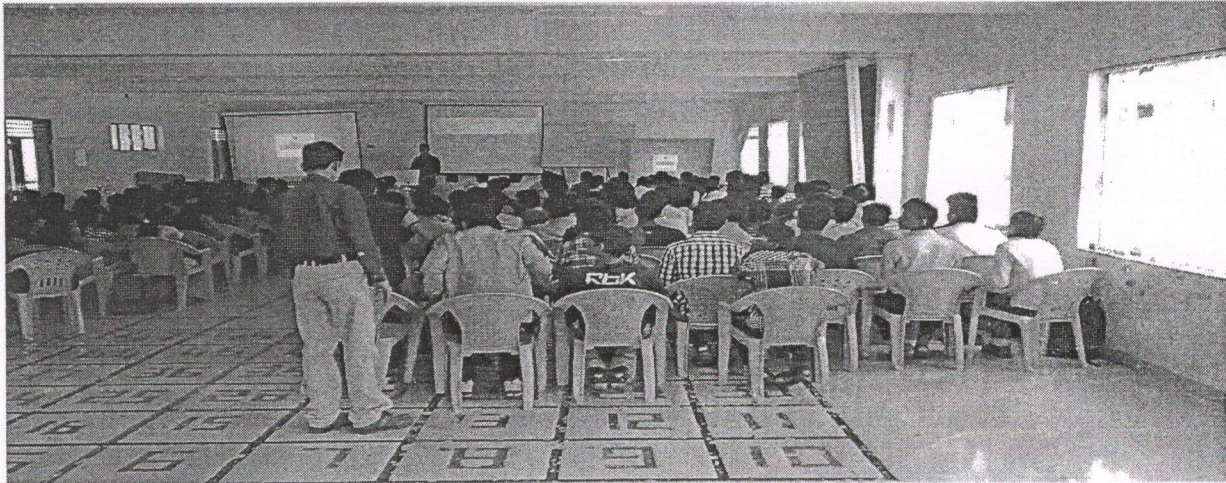
Dhangawadi, Pune-412206.

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**Department: Computer Engineering**

Red Hat Linux Workshop Photos





Rajgad Dnyanpeeth's

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**DEPT. OF ELECTRONICS AND TELECOMMUNICATION ENGG.**

**SEMINAR REPORT**

**DEPARTMENT:** E&TC

**CLASS:** TE & BE

**NAME OF GUEST:** Mr. Parth Joshi and Vrushali Khopkar

**TITLE/TOPIC:** “Career Opportunity After BE”.

**DATE:** 11<sup>th</sup> Aug 2014

**DURATION OF SESSION:** 11.00am

**STUDENT/STAFF PRESENT:** 82/5

**COORDINATOR:** Prof. M. S. Sapte

**OBJECTIVE:**. The main objective is to evoke new Career opportunities in among student. Also to guide the students about market requirements and the future scope.

**DESCRIPTION:** Engineers work in a variety of fields to analyze, develop and evaluate large-scale, complex systems. This can mean and improve and maintaining current systems or creating brand new projects. Engineers will design and draft blueprints, visit systems in the field and manage projects.

Electronic communications engineers conceptualize, design, test and oversee the manufacturing of communications and broadcast systems. They mainly work to integrate electronics and communications into any system they develop. These professionals might work for commercial, industrial, military or scientific companies.

A communications engineer is responsible for the research, design, development and production of communications equipment/systems. Communications engineering encompasses modes of communication such as satellites, radio, internet and broadband technologies and wireless telephone services.



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**CONCLUSION:** The seminar will guide students about the latest technology, market demand and new ideas will also ensure that students do make the right choice. Electronic communications engineers conceptualize, design, test and oversee the manufacturing of communications and broadcast systems.

**PHOTOS DURING SESSION :**



**Students during seminar**



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Mr. Joshi explaining the Opportunities

  
**Prof M. S. Sapte**  
Coordinator

  
**Prof. T. M. Dudhane**  
HOD



**Head of Department**  
Dept. of E& TC Engineering  
Shri Chh. Shivajiraje College of Engg.  
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**WORKSHOP REPORT**

**DEPARTMENT:** E&TC

**CLASS:** TE & BE

**NAME OF GUEST:** Mr. Aniket Mullur

**FROM:** Honeywell India Pvt Ltd

**TITLE/TOPIC:** “PIC, ARM and RTOS”.

**DATE:** 30<sup>th</sup> July 2014 – 04<sup>th</sup> Aug 2014

**DURATION OF SESSION:** 5 days

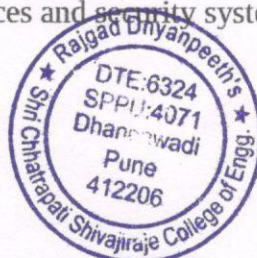
**STUDENT/ STAFF PRESENT:** 90/9

**OBJECTIVE:** The main objective is to learn the basics of low-cost and power efficient PIC, ARM 7 microcontroller. Introduce the outline architecture of PIC18FSS and ARM7 microcontroller including basics of pipelines, registers, exception modes, etc. Also how to set up and customize a microcontroller development environment and how to write programs that interact with other devices. Also to provide an introduction to the theory, structure and practice of real-time operating systems

**DESCRIPTION:** The PIC was designed to improve the performance of I/O operations from a computer's peripheral devices. It works as a standard microcontroller that has small processors, memory, registers and storage. Typically, a PIC enhances I/O operations from a peripheral devices by separating I/O-based programs and data from the core central processing unit (CPU).

A PIC has a built-in data memory, data bus and dedicated microprocessor for processing all I/O functions and processes. It consists of temporary and permanent storage mechanisms, in the form of random access memory (RAM) and erasable programmable read-only memory (EPROM), where RAM stores data/processes that are used and EPROM stores created values. It also may contain a flash memory, which is used to perform multiple instances of READ, WRITE and ERASE functions.

The Advanced RISC Machine has several microcontroller families among which ARM7 are the longest serving ARM processors. The ARM7 CPU core based microcontroller is the most popular 32-bit embedded processor which features a small microcontroller with low power consumption. We introduce students with the Philips' LPC2000 ARM7 based microcontroller, the first standard microcontroller to integrate ARM7 and Philips' new memory acceleration module. ARM architecture is used to almost every embedded application ranging from automotive communications protocol to medical devices and security systems.



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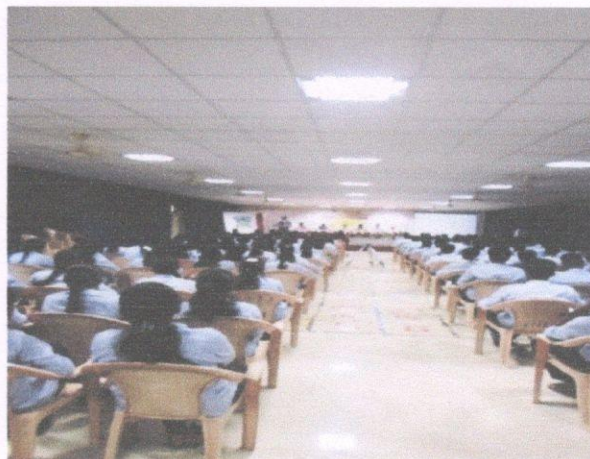
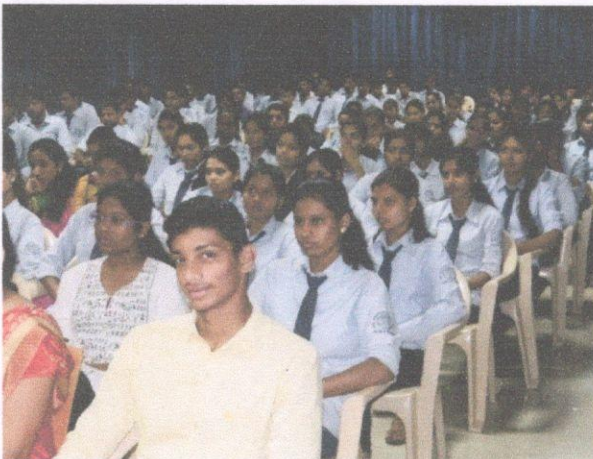
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A real-time operating system (RTOS) is an operating system that guarantees a certain capability within a specified time constraint. For example, an operating system might be designed to ensure that a certain object was available for a robot on an assembly line. In what is usually called a "hard" real-time operating system, if the calculation could not be performed for making the object available at the designated time, the operating system would terminate with a failure. In a "soft" real-time operating system, the assembly line would continue to function but the production output might be lower as objects failed to appear at their designated time, causing the robot to be temporarily unproductive. Some real-time operating systems are created for a special application and others are more general purpose

**CONCLUSION:** After workshop students are able to implement hardware of the PIC and ARM7 microcontrollers, understand integrated peripherals based on I/O functions, examples of internal peripheral software drivers. They are able to know concept of pipelines, registers and exception modes, instruction set. Also students understand the concept and application of RTOS. Students are also able to understand software development flow and working with projects.

### PHOTOS DURING SESSION



Students during Workshop

Prof. M. S. Sapte

Workshop Coordinator

Prof. T. M. Dudhane

HOD

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**SEMINAR REPORT**

**DEPARTMENT:** E&TC

**CLASS:** TE &BE

**NAME OF GUEST:** Mr. Choudhari and Sagar Jagtap

**TITLE/TOPIC:** “Career Growth and Opportunity in PLC”.

**DATE:** 10<sup>th</sup> July 2014

**DURATION OF SESSION:** 11.00am

**STUDENT/ STAFF PRESENT:** 83/5

**COORDINATOR:** Prof. D. U. Dalavi

**OBJECTIVE:**. The main objective is to evoke new Career opportunities in among student also to prepare our students for job opportunities in the field of industrial automation and plc controls. They incorporate technical skills from the electrical, mechanical, computer, and manufacturing disciplines.

**DESCRIPTION:** PLC Technicians install and repair industrial electronic equipment including I/O networks, data highways, variable speed drives, process control equipment, and write PLC programs for a wide variety of automated control systems ranging from simple on-off control to robotics. In addition to installing and maintaining programmable controllers, PLC Technicians also find employment in the industrial engineering field where they are actively involved in the design and implementation of PLC control systems.

Experienced PLC Technicians with advanced training may become specialists or troubleshooters who help other technicians diagnose difficult problems, or work with engineers in designing equipment and developing maintenance procedures.

PLC graduates find employment opportunities in process control, PLC programming, packaging, power generation, mining, machine design, and building automated systems, maintenance, transportation, systems integration, component testing, technical sales, quality control and a host of other fields.



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They also find jobs in a broad range of industries including automotive, pharmaceutical, food & beverage, chemical, oil and gas, waste water, etc.

**CONCLUSION:** The seminar will guide students to note that different sectors within manufacturing have diverse needs which will impact salary levels. Manufacturers in automotive and pharmaceuticals, for example, are more reliant on technology and R&D, therefore require more qualified and tech knowledgeable engineers.

**PHOTOS DURING SESSION :**



**Students during Workshop**

**Prof D. U. Dalavi**  
Coordinator

**Prof. T. M. Dudhane**  
HOD



**Head of Department**  
Dept. of E& TC Engineering  
Shri Chh. Shivajiraje College of Engg.  
Dhangawadi, Pune-412206

॥ प्रज्वालितो ज्ञानमयः प्रदीपः ॥



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**Department of Computer Engineering**

**Report of Seminar On “Software Testing”**

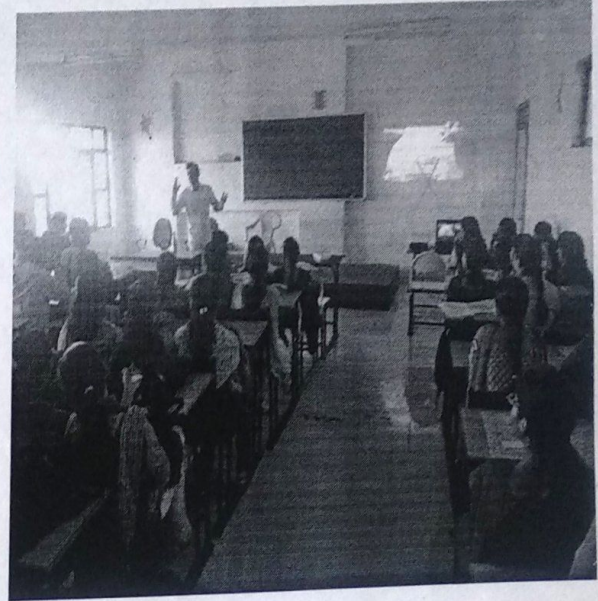
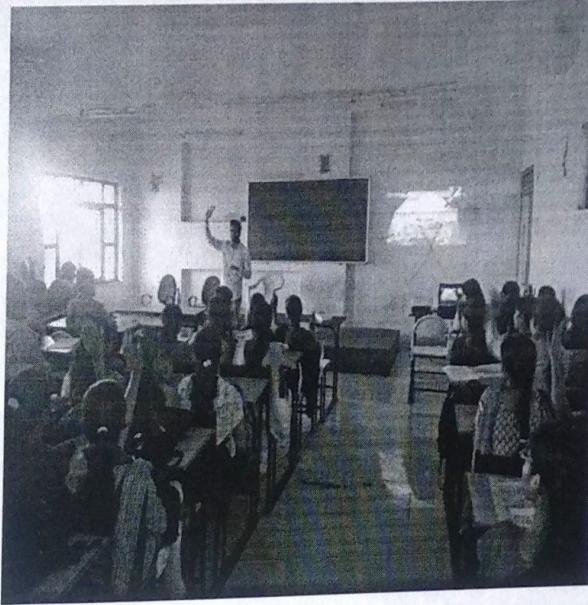
<u>DEPARTMENT</u>	:	Computer Engineering
<u>RESOURCE PERSON</u>	:	Mr. Hemant Pimple (Associate Consultant in Capgemini)
<u>TITLE/TOPIC</u>	:	“Software Testing”
<u>DATE</u>	:	15/03/2014
<u>DURATION OF EVENT</u>	:	10.00 AM to 5.00 PM
<u>STUDENT PRESENT</u>	:	44
<u>CO-ORDINATOR</u>	:	Prof. S. R. Bhuskute
<u>PROGRAM OBJECTIVE</u>	:	<i>“To understand the detail working of Java language in industry”.</i>
<u>DESCRIPTION</u>	:	This guidance will provide everything that student needs to know about Software Testing. More specifically they will learn about Testing performed in industry, its applications and types of testing using in Industry.
<u>PROGRAM OUTCOME</u>	:	<i>“Students will be able to understand how to handle the techniques and get awareness of its use in MNCs”.</i>



॥ प्रवृत्तितो ज्ञानमयः प्रदीपः ॥



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Department of Computer Engineering



*S.R.*  
Prof. S. R. Bhuskute  
Coordinator



*Bhagwan*  
Prof. B. D. Thorat  
HOD  
H.O.D  
DEPTT. OF COMPUTER ENGG.  
RDTC, Shri Chhatrapati Shivajiraje  
College of Engineering, Dhangwadi,  
Pune- 412206.



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Dhangwadi, Tal-Bhor, Dist. – Pune – 412206.  
**Department of Computer Engineering**

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**Report of Workshop On “Java Programming”**

<u>DEPARTMENT</u>	:	Computer Engineering
<u>RESOURCE PERSON</u>	:	Mr.Dipak Bardale (Sr. System Analyst in IBM)
<u>TITLE/TOPIC</u>	:	“Java Programming”
<u>DATE</u>	:	21/09/2013
<u>DURATION OF EVENT</u>	:	10.00 AM to 5.00 PM
<u>STUDENT PRESENT</u>	:	47
<u>CO-ORDINATOR</u>	:	Prof. A. S. Sondkar
<u>PROGRAM OBJECTIVE</u>	:	<i>“To understand the detail working of Java language in industry”.</i>
<u>DESCRIPTION</u>	:	This guidance will provide everything that student needs to know about java programming language. More specifically they will learn about features of java programming, its applications, reason to learn it and how you can use it the right way.
<u>PROGRAM OUTCOME</u>	:	“Students will be able to understand how to handle the techniques and get awareness of its use in MNCs”.



*A. S. Sondkar*

Prof. A. S. Sondkar  
Coordinator

*B. D. Thorat*

Prof. B. D. Thorat  
HOD

**Head of Department**  
Dept. of Computer Engineering  
Shri Chh. Shiyajiraje College of Engg.  
Dhangawadi, Pune-412206

Rajgad Dnyanpeeth's

**SHRI CHHATRAPATI SHIVAJIRAJE COLLEGE OF ENGINEERING**

Gat No. 237, Pune Bangalore Highway, Dhangawadi, Tal – Bhor, Dist- Pune (Maharashtra)

**WORKSHOP REPORT**

**DEPARTMENT:** E&TC

**CLASS:** TE & BE

**NAME OF GUEST:** Mr. Aniket Mullur

**FROM:** Honeywell India Pvt Ltd

**TITLE/TOPIC:** “ARM7 PROCESSOR”

**DATE:** 31.08.2013

**DURATION OF SESSION:** 11.00am

**STUDENT/ STAFF PRESENT:** 111/4

**OBJECTIVE:** The main objective is to learn the basics of low-cost and power efficient ARM 7 microcontroller. Introduce the outline architecture of ARM7 microcontroller including basics of pipelines, registers, exception modes, etc. Also how to set up and customize a microcontroller development environment and how to write programs that interact with other devices

**DESCRIPTION:** The Advanced RISC Machine has several microcontroller families among which ARM7 are the longest serving ARM processors. The ARM7 CPU core based microcontroller is the most popular 32-bit embedded processor which features a small microcontroller with low power consumption. We introduce students with the Philips' LPC2000 ARM7 based microcontroller, the first standard microcontroller to integrate ARM7 and Philips' new memory acceleration module. ARM architecture is used to almost every embedded application ranging from automotive communications protocol to medical devices and security systems.

**Philips LPC2000 series (The ARM7 CPU Core Based Microcontroller)**

**Outline architecture**

- The Pipeline
- Registers
- Current Program Status Register
- Exception Modes

**Accessing User Onchip Peripherals**

- General Purpose I/O
- General Purpose Timers
- Watchdog
- PWM Modulator
- Real Time Clock
- UART
- I2C Interface



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- SPI Interface
- Analog To Digital Converter
- Interrupt Service Routines
- Software Interrupt
- Hardware Debugging Tools

### Software Development

- uVision IDE: Embedded C
- Startup Code
- Interworking ARM/THUMB Code
- Locating Code In RAM
- Inline Functions
- Fixing Objects At Absolute Locations
- Inline Assembler

**CONCLUSION:** After workshop students are able to implement hardware of the ARM7 microcontrollers, understand integrated peripherals based on I/O functions, examples of internal peripheral software drivers. Also they are able to know concept of pipelines, registers and exception modes, ARM7 instruction set covering branching, data processing instructions, swap instruction, THUMB instruction set and others. Students are also able to understand software development flow and working with projects

### PHOTOS DURING SESSION



**Students During Workshop**



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**Mr. Aniket Mullur Explaining ARM 7 Processor**

**Prof. D. U. Dalavi**

**Workshop Coordinator**

**Prof. T. M. Dudhane**

**HOD**

**Head of Department**

**Dept. of E& TC Engineering  
Shri Chh. Shivajiraje College of Engg.  
Dhangawadi, Pune-412206**



**REPORT ON WORKSHOP****TITLE/TOPIC:** One day workshop on "Concrete Mix Design"**RESOURCE PERSON:** Sushant Inamdar, Project Head, Duracon Consultants, Pune.**DAY & DATE:** Saturday – 21 August 2013**VENUE:** Seminar hall, RDs, SCSCOE, Dhangawadi.**DURATION OF SESSION:** 10am to 5pm**STUDENT/STAFF PRESENT:** 80/08**CORDINATOR:** Asst. Prof. R. B. Kesarkar**OBJECTIVE:** The objective of proportioning concrete mixes is to arrive a most economical and practical combinations of the different ingredients.**DESCRIPTION:** The program was arranged by Department of Civil Engineering, Shri Chhatrapati Shivajiraje College of Engineering, Dhangawadi, Pune in association with Duracon Consultants. The program delivered by renowned and experienced personalities in the field.

The following course contents broadly covered by resource person:

- Mix design Overview
- Data for mix proportioning
- Selection of mix proportions
- Trial mixes
- Hands on training on concrete mix proportioning calculations
- Hands on training on preparing the designed concrete mix – Practical session

**CONCLUSION:** the workshop would be very useful to students and faculties for mix design of concrete with different grades.**PHOTO DURING SESSION:**

*R. B. Kesarkar*  
R. B. Kesarkar  
Workshop Coordinator



*G. S. Jadhav*  
G. S. Jadhav  
HOD  
Head of Department  
Dept. of Civil Engineering  
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**SEMINAR REPORT****DEPARTMENT:** E&TC**CLASS:** TE &BE**NAME OF GUEST:** Mr. Chinmay Oak**TITLE/TOPIC:** “Final Year Project Selection Guidelines”.**DATE:** 01<sup>st</sup> July 2013**DURATION OF SESSION:** 2 Hours**STUDENT/ STAFF PRESENT:** 135/6

**OBJECTIVE:** The main objective is to evoke new project ideas in among student. Also to guide the students about market requirements and the future scope.

**DESCRIPTION:** Project Selection is a process to assess each project idea and select the project with the highest priority. Projects are still just suggestions at this stage, so the selection is based on only brief descriptions of the project, market requirement and latest technologies.

The final year project is the culmination of the degree – it gives students a chance to demonstrate all they have learned. The project module is very different from other modules. The project tests students' ability to: design, engineer and evaluate quality systems.

The project engineer is also often the primary technical point of contact for the consumer. A project engineer's responsibilities include schedule preparation, pre-planning and resource forecasting for engineering and other technical activities relating to the project.

Project managers in engineering are responsible for leading and directing specialized engineers in specific projects. Strong communication skills and the ability to think quickly to resolve problems in real time are essential in this position, and organizational skills and close attention to detail are also important.

There are various project selection methods practiced by the organizations. These methods have different features and characteristics. Therefore, each selection method should be best for different organizations. Although there are many differences between these project selection methods, usually the underlying concepts and principles are the same.

**CONCLUSION:** It is always best to have a good plan from the inception, with a list of criteria to be considered and goals to be achieved. The seminar will guide students about the latest technology, market demand and new ideas will also ensure that students do make the right choice





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**PHOTOS DURING SESSION :**



Mr. Chinmay Oak Guiding Students



Students during seminar

**Prof. D.U. Dalavi**  
Coordinator

**Prof. T. M. Dudhane**  
HOD

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