



Rajgad Dnyanpeeth's  
**SHRI CHHATRAPATI SHIVAJIRAJE COLLEGE OF ENGINEERING**  
Gat No. 237, Pune Bangalore Highway, Dhangawadi, Tal – Bor, Dist- Pune (MH)

**Criterion 2 : Teaching Learning & Evaluation**  
**Key Indicator : 2.4 Teacher Profile and quality**

2.4.5 Average percentage of full time teachers from other States against sanctioned Posts

Academic Year	2013-14	2014-15	2015-16	2016-17	2017-18	2018-19
No of staff	4	5	6	8	7	9

**Principal**  
Rajgad Dnyanpeeth's  
Shri Chhatrapati Shivajiraje College of Engg.,  
Dhangawadi, Pune-412206





Rajgad Dnyanpeeth's

**SHRI CHHATRAPATI SHIVAJIRAJE COLLEGE OF ENGINEERING**

Gat No. 237, Pune Bangalore Highway, Dhangawadi, Tal – Bor, Dist- Pune (Maharashtra)

**Criterion 2 : Teaching Learning & Evaluation**

**Key Indicator : 2.4 Teacher Profile and Quality**

**2.4.5 Average percentage of full time teachers from other States against sanctioned posts**

Name of full time teacher from other state	State from which qualifying degree was obtained	State from which qualifying degree was obtained
Mr. Dayanand U Dalavi	Completed M.TECH from JNTU, Hyderabad, Andhra Pradesh	Completed M.TECH from JNTU, Hyderabad, Andhra Pradesh
Mr. Gorakhanath S. Jadhav	Completed M.TECH from IIT , Kharagpur, WestBangal	Completed M.TECH from IIT , Kharagpur, WestBangal
Ms. Priti R. Bhalchakra	Completed B.E from Bhopal, Madhya Pradeh	Completed B.E from Bhopal, Madhya Pradeh
Mr.Ramesh.G Patole	Completed M.TECH from JNTU, Hyderabad, Andhra Pradesh	Completed M.TECH from JNTU, Hyderabad, Andhra Pradesh
Mrs. Jyoti V Chavan	Completed M.TECH from JNTU, Hyderabad, Telgana	Completed M.TECH from JNTU, Hyderabad, Telangana
Mr. Lakhan M Sagale	Completed M.TECH from JNTU, Hyderabad, Andhra Pradesh	Completed M.TECH from JNTU, Hyderabad, Andhra Pradesh
Ms..Deepika.P .Patil	Completed M.E (CSE) from, Bhopal, Madhya Pradesh	Completed M.E (CSE) from, Bhopal, Madhya Pradesh
Ms. Khushbu B Doulani .	Completed M.Tech from Dr A.P.J abdul Kalam Technical university,	Completed M.Tech from Dr A.P.J abdul Kalam Technical university,
Mrs. Tanuja S. Hukkeri	Completed B.E & MTECH from VTU, Belgaum, Karnataka	Completed B.E & MTECH from VTU, Belgaum, Karnataka

Mr. Mukund B Wagh	Completed M.TECH from JNTU, Hyderabad, Andhra Pradesh	Completed M.TECH from JNTU, Hyderabad, Andhra Pradesh
Mr.S.I.Nipanikar	Completed PhD from, VELTECH university, Tamilnadu	Completed PhD from, VELTECH university, Tamilnadu
Mr.G..S.Kothawale	Completed M.TECH from JNTU, Hyderabad, Telgana	Completed M.TECH from JNTU, Hyderabad, Telgana



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD  
HYDERABAD - 500 085, ANDHRA PRADESH, INDIA

College: M3 - N C E T, LEMOOR

30198002213

ITT No: 11M31D5502

Sl. No. PC 00467278



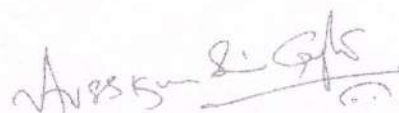
PROVISIONAL CERTIFICATE

20198002217

This is to certify that Mr. DALAVI DAYANAND UTTAM  
son of Mr. DALAVI UTTAM VISHWANATH  
passed M. Tech. (EMBEDDED SYSTEMS) degree  
examination of this University, held in December, 2013 and that he was  
placed in FIRST CLASS WITH DISTINCTION.

He has satisfied all the requirements for the award of the degree.

Hyderabad - A.P.

  
Controller of Examinations

  
REGISTRAR

Date: January 16, 2014





INDIAN INSTITUTE OF TECHNOLOGY, KHARAGPUR-751002

STATEMENT OF GRADES OBTAINED FOR THE 4 SEMESTER POST GRADUATE PROGRAMME LEADING TO THE DEGREE OF  
MASTER OF TECHNOLOGY(M.Tech)



Roll No.: 06CE6305

Name: JADHAV GORAKHANATH SADASHIV

Year of Admission: 2006

Department/Centre: CIVIL ENGINEERING

Year of Passing: 2008

Specialisation: ENVIRONMENTAL ENGINEERING & MANAGEMENT

Semester 1

Sub No.	Subject Name	LTP	CRD	GRD
CE60021	WATER SUPPLY SYSTEMS	4-0-0	4	B
CE60023	WASTEWATER MANAGEMENT	4-0-0	4	B
CE60067	ENVIRONMENTAL CHEMISTRY AND MICROBIOLOGY	3-1-0	4	A
CE60097	SOLID WASTE MANAGEMENT	3-1-0	4	B
CE69001	SEMINAR-I	0-0-2	1	B
CE69011	ENVIRONMENTAL MONITORING LAB - I	0-0-3	2	A
CE69013	PLANNING AND DESIGN OF ENV. ENGG. FACILITIES	0-0-3	2	A
HS60001	MANAGEMENT OF HUMAN RESOURCES	3-1-0	4	B

SGPA: 8.32

CGPA: 8.32

Semester 2

Sub No.	Subject Name	LTP	CRD	GRD
CE60022	AIR QUALITY MANAGEMENT	4-0-0	4	B
CE60024	INDUSTRIAL WATER POLLUTION CONTROL	4-0-0	4	EX
CE60072	ADVANCED WATER & WASTEWATER TREATMENT	3-1-0	4	C
CE60076	HAZARDOUS WASTE MANAGEMENT	3-1-0	4	B
CE60078	ENVIRONMENTAL IMPACT ASSESSMENT	3-1-0	4	B
CE68002	COMPREHENSIVE VIVA VOCE	0-0-0	3	A
CE69002	SEMINAR-II	0-0-2	1	A
CE69008	ENVIRONMENTAL MONITORING LAB II	0-0-3	2	A

SGPA: 8.38

CGPA: 8.35

Semester 3

Sub No.	Subject Name	LTP	CRD	GRD
CE67001	PROJECT (PART-I)	0-0-0	15	EX

SGPA: 10.00

CGPA: 8.73

Semester 4

Sub No.	Subject Name	LTP	CRD	GRD
CE67002	PROJECT (PART-II)	0-0-0	20	EX

SGPA: 10.00

CGPA: 9.02

Total Credits required for Course Completion: 80 - 86

Total Credits Taken: 86

Total Credits Cleared: 86

Date of Issue: 20 Jun 2008

*Shah*

Checked by Superintendent(PGS and R)

*W. S. S.*

Deputy Registrar (Academic)/Asst Registrar (PGS and R)

# Indian Institute of Technology Kharagpur



*upon the recommendation of the Senate  
hereby confers the degree of*

**Master of Technology**

*in*

**Civil Engineering**

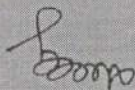
*(Specialisation : Environmental Engineering & Management)*

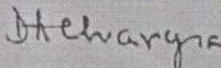
*on*

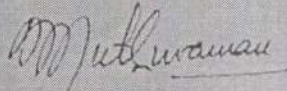
**Jadhav Gorakhanath Sadashiv**

*who has successfully completed the prescribed course of studies in the  
academic session 2007 - 2008.*

*Given under the seal of the Institute at Kharagpur in the Republic of  
India on the Twentyfifth day of July, Two thousand eight.*

  
Registrar

  
Director

  
Chairman  
Board of Governors

https://mail.google.com/mail/u/0/#inbox/FNDWMGkHspczjKHsIbTwnBjoxNFjvZ?projector=1&messagePartId=0.1



# RAJIV GANDHI PROUDYOGIKI VISHWAVIDYALAYA, BHOPAL

(UNIVERSITY OF TECHNOLOGY OF MADHYA PRADESH)

## STATEMENT OF MARKS EXAMINATION JUNE - 2006

SR. No. 849776

ENROLMENT NO. : 0201ME011057

NAME : PREETI BHALCHAKRA

B.E., (Mechanical Engineering)

SEMESTER : VIII

INSTT. : Jabalpur Engineering College, Jabalpur



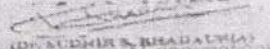
STATUS : Regular

CODE	SUBJECTS	MAXIMUM MARKS						MARKS OBTAINED					
		THEORY BLOCK			PRACT. BLOCK			THEORY BLOCK			PRACT. BLOCK		
		END SEM	MD SEM	TOTAL	PRACT. WORK	TERM WORK	TOTAL	END SEM	MD SEM	TOTAL	END SEM	TERM WORK	TOTAL
ME801	Machine Design-II	100	20	120	50	30	80	51	20	71	41	22	63
ME802	Refrigeration & Air Conditioning	100	20	120	50	30	80	68	10	78	45	22	67
ME803	Computer Integrated Mfg.	100	20	120	50	30	80	63	15	78	37	26	63
ME8301	Elective-III	100	20	120	—	—	—	63	19	82	—	—	—
ME804	Major Project	—	—	—	100	100	200	—	—	—	87	95	182
ME805	Self Study	—	—	—	—	30	30	—	—	—	—	28	28
ME806	Seminar & Group Discussion	—	—	—	—	50	50	—	—	—	—	45	45
<b>TOTAL</b>		<b>400</b>	<b>80</b>	<b>480</b>	<b>250</b>	<b>270</b>	<b>520</b>	<b>245</b>	<b>64</b>	<b>309</b>	<b>210</b>	<b>238</b>	<b>448</b>

WORDS SEVEN FIVE SEVEN ... OUT OF 1000.

GRAND TOTAL 757

I & II : 120/200    III & IV : 234/400    V & VI : 350/600    VII & VIII : 1441/2000    AGGREGATE : 2145/3200

10/6/2006    JABALPUR    849776



**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD**  
**HYDERABAD - 500 085, ANDHRA PRADESH, INDIA**



**CONSOLIDATED MARKS MEMO / CREDIT SHEET**

**Master of Technology- SOFTWARE ENGINEERING**

CAM.No: **C 0362199**

Name of the College: **N C E T L E M O O R**

Serial No: **20192002205**

Month & Year of Final Exam: **December, 2013**

Name: **PATOLE RAMESH GANPAT**

Class Awarded: **FIRST CLASS WITH DISTINCTION**

Roll Ticket No: **11MS1D2508**

Year of Admission: **2011-2012**

S.No	SUBJECT TITLE	BT	TE	Total	Passing	Status
		Marks	Marks			
	Maximum Marks in Theory	40	60	100		
	Maximum Marks in Lab	40	60	100		

**I SEMESTER**

1	SOFTWARE REQUIREMENTS AND ESTIMATION	34	30	64	P
2	OBJECT ORIENTED MODELING	31	37	68	P
3	SOFTWARE PROCESS AND PROJECT MANAGEMENT	32	48	80	P
4	JAVA AND WEB TECHNOLOGIES	33	32	65	P
5	ADVANCED COMPUTER NETWORKS	29	27	56	P
6	INFORMATION RETRIEVAL SYSTEMS	35	32	67	P
7	JAVA AND WEB TECHNOLOGIES LAB	30	45	75	P
8	SEMINAR	42	-	42	P

**II SEMESTER**

1	DISTRIBUTED COMPUTING	32	32	64	P
2	SOFTWARE QUALITY ASSURANCE AND TESTING	32	39	71	P
3	SOFTWARE ARCHITECTURE AND DESIGN PATTERNS	32	45	77	P
4	SERVICE ORIENTED ARCHITECTURE	31	45	76	P
5	SEMANTIC WEB AND SOCIAL NETWORKS	32	44	76	P
6	IMAGE PROCESSING AND PATTERN RECOGNITION	30	39	69	P
7	UML AND SOFTWARE TESTING LAB	34	50	84	P
8	SEMINAR - II	37	-	37	P

**III SEMESTER**

1	COMPREHENSIVE VIVA - VOCE	-	85	85	P
(Comprehensive VIVA - External- 100)					

**IV SEMESTER**

1	PROJECT WORK*	-	B	-	P
(*A-Excellent, B-Good, C-Satisfactory, D-Un Satisfactory)					

Aggregate Marks Secured: **1156 OUT OF 1800 (72.25%)**

Date of Issue: **January 16, 2014**  
(Date overleaf for Rules concerned by award of class)



(\*Courses registered but not counted for calculation of aggregate)

*A. S. S. S.*  
**CONTROLLER OF EXAMINATIONS**





JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

HYDERABAD - 500 085, ~~ANDHRA PRADESH~~, INDIA

College: M5 - R R S C E T, PATANCHERU TELANGANA STATE

30210011144

HT No: 12M51D3817

SI. No. PC 00698579



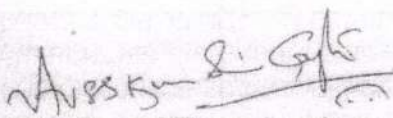
PROVISIONAL CERTIFICATE

20210011129

This is to certify that Mr. SAGALE LAKHAN MARUTI  
son of Mr. MARUTI BABU RAO  
passed M. Tech. (DIGITAL ELECTRONICS & COMMUNICATION SYSTEMS) degree  
examination of this University, held in March, 2015 and that he was  
placed in FIRST CLASS WITH DISTINCTION.

He has satisfied all the requirements for the award of the degree.

T.S.  
Hyderabad - XX.

  
Controller of Examinations

  
REGISTRAR

Date: April 3, 2015





Master of Technology- DIGITAL ELECTRONICS & COMMUNICATION SYSTEMS

CMM No. C 0564410

Serial No. 20210011129

Name SAGALE LAKHAN MARUTI

Name of the College R R S C E T, PATANCHERU

Month & Year of Final Exam: March, 2015

Hall Ticket No. 12M51D3817

Year of Admission: 2012-2013

Class Awarded: FIRST CLASS WITH DISTINCTION

S.No	SUBJECT TITLE	INT	EXT	Total	Pass(P) Fail(F)
		MARKS	MARKS	MARKS	
	Maximum Marks in Theory	40	60	100	
	Maximum Marks in Lab	40	60	100	

I SEMESTER

1	DIGITAL SYSTEM DESIGN	37	35	72	P
2	ADVANCED DIGITAL SIGNAL PROCESSING	36	32	68	P
3	VLSI TECHNOLOGY AND DESIGN	37	24	61	P
4	ADVANCED DATA COMMUNICATIONS	36	32	68	P
5	MICROCONTROLLERS FOR EMBEDDED SYSTEM DESIGN	34	46	80	P
6	EMBEDDED REAL TIME OPERATING SYSTEMS	36	36	72	P
7	SIGNAL PROCESSING LAB	00	55	55	P
8	SEMINAR	45	-	45	P

II SEMESTER

1	IMAGE & VIDEO PROCESSING	37	34	71	P
2	CODING THEORY AND TECHNIQUES	36	27	63	P
3	OPTICAL COMMUNICATIONS TECHNOLOGY	37	37	74	P
4	WIRELESS COMMUNICATIONS & NETWORKS	35	41	76	P
5	LOW POWER VLSI DESIGN	37	30	67	P
6	NETWORK SECURITY & CRYPTOGRAPHY	39	32	71	P
7	ADVANCED COMMUNICATIONS LAB	37	56	93	P
8	SEMINAR - II	48	-	48	P

III SEMESTER

1	COMPREHENSIVE VIVA - VOCE	-	95	95	P
(Comprehensive VIVA External= 100)					

IV SEMESTER

1	PROJECT WORK*	-	B	-	P
(* A - Excellent, B - Good, C - Satisfactory, D - Un Satisfactory)					

Aggregate Marks Secured

Date of Issue: 1179 OUT OF 1600 (73.69%)

(see overleaf for Rules to be followed by students of class)

April 8, 2015

(\*Courses registered but not counted for awarding of degree)



CONTROLLER OF EXAMINATIONS



**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD**  
HYDERABAD - 500 085, TELANGANA STATE, INDIA.

**CONSOLIDATED MARKS MEMO / CREDIT SHEET**

M.Tech. VLSI SYSTEM DESIGN

MM. No.: **C 0635748**

Serial No.: 20210011599

Name: **CHAVAN JYOTI VISHWAS**

Hall Ticket No.: **12M31D5702**



Name of the College: **N C E T, LEMOOR**

Month & Year of Final Exam: **May, 2015**

Year of Admission: **2012-2013**

Class Awarded: **FIRST CLASS**

S.No	SUBJECT TITLE	INT	EXT	Total	Pass(P)/ Fail(F)
		MARKS	MARKS	MARKS	
	Maximum Marks in Theory	40	60	100	
	Maximum Marks in Lab	40	60	100	

**I SEMESTER**

1	MICROCONTROLLERS FOR EMBEDDED SYSTEM DESIGN	12	39	51	P
2	CPLD & FPGA ARCHITECTURES AND APPLICATIONS	15	35	50	P
3	VLSI TECHNOLOGY & DESIGN	30	26	56	P
4	ALGORITHMS FOR VLSI DESIGN AUTOMATION	26	37	63	P
5	DIGITAL SYSTEM DESIGN	26	25	51	P
6	ADVANCED DIGITAL SIGNAL PROCESSING	30	24	54	P
7	SIMULATION LAB (VLSI)	32	43	75	P
8	SEMINAR	37	-	37	P

**II SEMESTER**

1	SYSTEM ON CHIP ARCHITECTURE	31	24	55	P
2	CMOS ANALOG & MIXED SIGNAL DESIGN	32	28	60	P
3	EMBEDDED REAL TIME OPERATING SYSTEMS	33	26	59	P
4	DESIGN OF FAULT TOLERANT SYSTEMS	30	41	71	P
5	DIGITAL SIGNAL PROCESSORS AND ARCHITECTURES	29	34	63	P
6	LOW POWER VLSI DESIGN	29	32	61	P
7	EMBEDDED SYSTEMS DESIGN LAB	38	55	93	P
8	SEMINAR - II	36	-	36	P

**III SEMESTER**

1	COMPREHENSIVE VIVA - VOCE	-	78	78	P
(Comprehensive VIVA External= 100)					

**IV SEMESTER**

1	PROJECT WORK*	-	C	-	P
(* A - Excellent, B - Good, C - Satisfactory, D - Un Satisfactory)					

Aggregate Marks Secured: **1013 OUT OF 1600 (63.31%)**

Date of Issue: **August 23, 2015**

(see overleaf for Rules concerned to award of class)



(\*Courses registered but not counted for calculation of aggregate)

CONTROLLER

माध्यमिक शिक्षा मण्डल, मध्यप्रदेश, भोपाल  
BOARD OF SECONDARY EDUCATION, MADHYA PRADESH, BHOPAL



हाई स्कूल सर्टिफिकेट परीक्षा (10+2) 2001  
HIGH SCHOOL CERTIFICATE EXAMINATION (10+2) 2001

प्रमाणपत्र-सह अंकसूची स.क्र./S.No.  
CERTIFICATE-CUM MARKSHEET  
MARCH-APRIL, 2001

024660263

केन्द्र क्रमांक CENTRE NO.	संस्था क्रमांक SCHOOL No.	पंजीयन/नामांकन क्रमांक REGISTRATION/ ENROLMENT NO.	नियमित/स्वाध्यायी REGULAR/ PRIVATE	अनुक्रमांक ROLL NUMBER
53052	532104	5313432	REGULAR	15322940



प्रमाणित किया जाता है कि  
CERTIFIED THAT

श्री/श्रीमती/कुमारी :  
SRI/SMT/KUM DEEPIKA PATIL

जिसके

WHOSE

पिता/पति का नाम श्री :  
FATHER'S/HUSBAND'S NAME IS SRI PRADIP PATIL

माता का नाम श्रीमती :  
MOTHER'S NAME IS SMT KALPANA PATIL

तथा जन्मतिथि :  
AND DATE OF BIRTH IS 20/12/1985 TWENTYTH DECEMBER NINETEEN EIGHTY FIVE

हाईस्कूल सर्टिफिकेट परीक्षा में संस्था/केन्द्र\*\*  
से सम्मिलित हुए  
HAS APPEARED IN THE HIGH SCHOOL CERTIFICATE EXAMINATION FROM  
\*\*SCHOOL/CENTRE SHRI VAISHNAV GIRL'S H.S.SCHOOL INDORE  
एवं उनका परीक्षाफल रहा।

AND HAS BEEN DECLARED PASS IN SECOND DIVISION

उनके द्वारा विषयवार निम्नानुसार प्राप्तांक अर्जित किए गए :-

SUBJECTWISE MARKS OBTAINED BY HIM/HER ARE AS UNDER :-

विषय/SUBJECTS	अधिकतम अंक MAX. MARKS	न्यूनतम उत्तीर्णांक MIN. PASS MARKS		प्राप्तांक / MARKS OBTAINED			विवरण REMARKS
		सैद्धांतिक THEORY	प्रायोगिक PRACTICAL	सैद्धांतिक THEORY	प्रायोगिक PRACTICAL	योग TOTAL	
HINDI (SPECIAL)	100	33	-	064	-	064	✓
ENGLISH (GENERAL)	50	17	-	030	-	030	
SANSKRIT (GENERAL)	50	17	-	030	-	030	
MATHEMATICS	100	33	-	055	-	055	✓
SCIENCE	100	25	08	036	021	057	
SOCIAL SCIENCE	100	33	-	054	-	054	
	500					290	← महायोग GRAND TOTAL

महायोग शब्दों में  
TOTAL IN WORDS : TWO HUNDRED NINETY \*\*\*\*\*

(केवल नियमित छात्र/छात्रा के लिए For Regular Students only)

निम्नांकित आंतरिक विषयों में निपुणता प्राप्त की :-

Attained Proficiency in the following internal subjects :-

- (1) समाजोपयोगी उत्पादक कार्य Socially useful Productive work.
- (2) शारीरिक एवं नैतिक शिक्षा Physical and Moral Education.

5304074

(क.प.उ.देखें)

TRUE COPY

Dipali Pasrogi  
सचिव/SECRETARY

HEAD MASTER

Keshavnagar Bhaurao Patil Vidyamandir.  
Dhankawadi, Pune-43

प्रधान के स्वामी से हस्ताक्षर एवं पत्र मुद्रा  
Seal and Signature of Pradip Patil  
श्री प्रदीप प्रदीप  
प्रधान  
माध्यमिक शिक्षा मण्डल, भोपाल

राजीव गाँधी प्रौद्योगिकी विश्वविद्यालय  
RAJIV GANDHI PROUDYOGIKI VISHWAVIDYALAYA  
(UNIVERSITY OF TECHNOLOGY OF MADHYA PRADESH)



क्रमांक  
(Serial No.) 0024595

अनुक्रमांक 27430  
(Roll No.)

पत्रोपाधि प्रमाण-पत्र

यह प्रमाणित किया जाता है कि कुमारी दीपिका पाटिल

ने शामकीय महिला पोलिटेक्निक बुरहानपुर

इन्फार्मेशन टेक्नालॉजी

में

तीन वर्षीय पत्रोपाधि/उत्तर पत्रोपाधि कार्यक्रम पूर्ण कर, माह मई/जून 2004

में सम्पन्न हुई परीक्षा प्रथम श्रेणी ( /-/-/-/-/-/-/ ) में उत्तीर्ण की।

उक्त कार्यक्रम सफलतापूर्वक पूर्ण करने पर इन्हें यह पत्रोपाधि/उत्तर पत्रोपाधि प्रदत्त की जाती है।

DIPLOMA CERTIFICATE

This is to certify that **KUMARI DEEPIKA FATIL**

of **GOVT. WOMEN'S POLYTECHNIC BURHANPUR**

having completed **THREE** years Diploma/Post Diploma Programme in

**INFORMATION TECHNOLOGY**

passed the Examination held in the month of **MAY/JUNE 2004** in **FIRST**

Division ( /-/-/-/-/-/-/ ).

This Diploma/Post Diploma is awarded to him/her after successfully completing the above Programme.

सचिव  
(SECRETARY)

Bhopal, Dated the

TRUE-COPY

HEAD MASTER

Kemurji Bhauroo Patil Vidyamandir,  
Dhankawadi, Pune-43

कुलपति  
(VICE-CHANCELLOR)

4 APR 2005

# RAJIV GANDHI PROUDYOGIKI VISHWAVIDYALAYA, BHOPAL



No. : 074349

(University of Technology of Madhya Pradesh)  
INDIA

Date : 29/03/2010

## PROVISIONAL DEGREE CERTIFICATE

This is to certify that Mr./Ms. DEEPIKA PATIL

Son/Daughter of Mr./Mrs. PRADEEP PATIL

Enrolment No. 0817IT063D02 who was a student at Rishi Raj Institute of Technol -  
ogy, Indore has completed all the requirements and has

become eligible for award of BE, Information Technology

in DEC-2009 and is placed in FIRST Division.

**The degree shall, however, be conferred at the forthcoming convocation.**



डॉ. सजय बा. जगताप  
विशेष कार्यकारी अधिकारी  
स. नं. ३४, भारती विद्यापीठ मार्ग,  
धनकवडी, पुणे-४११ ०४३.

**RAJIV GANDHI PROUDYOGIKI VISHWAVIDYALAYA**

Airport Bypass Road, Gandhi Nagar, Bhopal - 462 036.

Phone : (0755) 2678891, 2678832, 2678855.

(Dr. A.K.S. Bhadoria)  
Registrar



**RAJIV GANDHI PROUDYOGIKI VISHWAVIDYALAYA, BHOPAL**  
(UNIVERSITY OF TECHNOLOGY OF MADHYA PRADESH)

**STATEMENT OF GRADE**

SR. No. 0749259

EXAMINATION DEC-2012

ME, (Computer Science & Engg.)

ROLL NO. : 0829CS11ME05  
NAME : DEEPIKA PATIL  
S/D/WO PRADEEP PATIL



INSTT. : Sushila Devi Bansal College of Technology, Indore,  
SEMESTER : III

STATUS : Regular

SUBJECT CODE	SUBJECT NAME	CREDITS		GRADE
		TOTAL CREDIT	CREDIT EARNED	
MCSE301	Elective-I(Data Mining & Ware Housing)	4	4	B
MCSE302	Elective-II(Network Security)	4	4	C+
MCSE303	Seminar	4	4	B+
MCSE304	Dissertation Part-I	8	8	B+
TOTAL		20	20	

RESULT : PASS SGPA : 7.40 CGPA (As on Date) : 6.95

Issue Date : 14/6/2013

Prepared By

Checked By

DIRECTOR  
Signature of Principal  
Sushila Devi Bansal College  
of Technology, Indore

Controller of Examinations  
(RGPV, Bhopal)

749259

डॉ. संजय वा. जगताप  
विशेष कार्यकारी अधिकारी  
स. सं. ३४, भारतीय विद्यापीठ माले,  
धनसदवडी, पुणे-४११ ०४३.



# SANT LONGOWAL INSTITUTE OF ENGINEERING & TECHNOLOGY

(DEEMED TO BE UNIVERSITY)

LONGOWAL-148 106, DIST. SANGRUR, PUNJAB, INDIA

(ESTABLISHED BY GOVERNMENT OF INDIA)



## Diploma

This is to Certify that Mr./Ms. KHUSHBU DOULANI

son/daughter of Shri. BINDU KUMAR

having completed the prescribed Two Years Diploma Course in  
COMPUTER SCIENCE & ENGINEERING

in the year  
2007 at this institute & passed in FIRST Division, is hereby awarded Diploma.

By Registrar (Academics)

Dean (Academics)

Director

Serial Number : 503732

Registration Number : GCS-8277/07

# SANT LONGOWAL INSTITUTE OF ENGINEERING AND TECHNOLOGY

(Deemed-To-Be-University)  
LONGOWAL-148106, SANGRUR, PUNJAB, INDIA  
(Established By: Govt. of India)



## BACHELOR OF ENGINEERING

This is to certify that KHUSHBU DOULANI

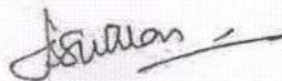
Son/Daughter of Sh. BINDU KUMAR

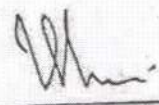
has completed the Requirements for the award of Degree of Bachelor of Engineering in  
COMPUTER SCIENCE & ENGINEERING of this institute in

FIRST Division, with Cumulative Grade Point Average (CGPA) 7.32 out

of 10.00 and is equivalent to 72.71 % of marks.

  
Deputy Registrar  
(Academics)

  
Dean  
(Academics)

  
Director

**SANT LONGOWAL INSTITUTE OF ENGINEERING & TECHNOLOGY  
LONGOWAL-148 106, DISTT. SANGRUR(PUNJAB)**

(Deemed to be University)  
(Established by Govt. of India)



**CLUBBED RESULT CARD**

**BACHELOR OF ENGINEERING**

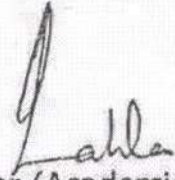
NAME OF COURSE : COMPUTER SCIENCE & ENGINEERING  
NAME OF THE STUDENT : KHUSHBU DOULANI  
FATHER'S NAME : BINDU KUMAR  
MOTHER'S NAME : RAJNI DOULANI

**DETAIL OF SEMESTER WISE SGPA**

S. No.	Semester	SGPA on 10 Point Scale	Credits
1	1 <sup>st</sup>	7.00	28
2	2 <sup>nd</sup>	5.07	28
3	3 <sup>rd</sup>	8.37	27
4	4 <sup>th</sup>	7.85	27
5	5 <sup>th</sup>	7.20	25
6	6 <sup>th</sup>	8.43	23
7	Industrial Training	10.00	02
8	General Proficiency	7.67	06


Result : Passed with CGPA 7.32 on 10 Point Scale.

Date : 29.07.2010  
Place : Longowal

  
Dy. Registrar (Academics)

[Home](#)

## Dr. A.P.J. Abdul Kalam Technical University, U.P., Lucknow

Roll No:	1342810508	
Name:	KHUSHBU DOULANI	
Father's Name:	BINDU KUMAR	
Enrollment No.:	PROVISIONAL	
Course:	M.Tech.(02)	
Branch:	Computer Science & Engineering	
Institute Name (Code):	Kashi Institute Of Technology,Varanasi(428)	

## M.Tech. First Year Result (REGULAR) (2013-14)

1 Semester Marks details						
Paper Code	Paper Name	External Marks	Sessional Marks	Carry Over	Credit	
CS11	Foundations of Computer Science	049	043			
CS12	Computer Organization and Architecture	044	045			
CS13	OS and DBMS	050	043			
CS14	Data Networks	070	043			
Practical						
Total Credits Earned		Practical: 0	Theory: 0			
		Status		Marks/Max. Marks		
		PASS		387/ 600		
Date Of Declaration of Result (Even): 07-04-15						
Date Of Declaration of Result (Odd): 29-09-15						

Note : 1. As per govt. notification no. 3324/solah-1-2013-1(3)/2009 dated 31.10.2013, Mahamaya University,Noida has been merged with Gautam Buddh Technical University, Lucknow and the University established after such merger shall be called, Uttar Pradesh Technical University Lucknow .

2. As per govt. notification no. 2696/solah-1-2015-1(3)/2009 dated 16.09.2015, Uttar Pradesh Technical University, Lucknow, has been renamed as Dr. A.P.J. Abdul Kalam Technical University Uttar Pradesh, Lucknow.

## M.Tech. Result (REGULAR) (2013-14)

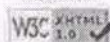
## M.Tech. Third Semester Result (REGULAR) (2014-15)

## M.Tech. Fourth Semester Result (Final Year) ( ) (2014-15)

Note: University does not own for the errors or omissions, if any, in this statement.

[Back](#)

This is the official Website of Dr. A.P.J. Abdul Kalam Technical University, Lucknow, State Government of Uttar Pradesh (India). | Best viewed in 1024\*768 pixel resolution.



ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ  
VISVESWARAIAH TECHNOLOGICAL UNIVERSITY, BELGAUM  
KARNATAKA, INDIA



Certifies that

**TANUJA L HUKKERI**

ಬ್ಯಾಚಲರ್ ಆಫ್ ಇಂಜಿನಿಯರಿಂಗ್

ಪದವಿಗೆ ಅಗತ್ಯವಾದ ಅರ್ಹತೆಗಳನ್ನು ಳ್ಳವರೆಂದು ಪರಿಗಣಿತವಾಗಿರುವ ಕಾರಣ  
ಮೇಲೆ ಕಾಣಿಸಿದ ಪದವಿಗೆ ಅವರನ್ನು ಅಂಗೀಕರಿಸಲಾಗಿದೆ.

*has been duly admitted to the Degree of*  
**Bachelor of Engineering**  
*in recognition of the fulfilment of requirements*  
*for the said degree*



ಪರೀಕ್ಷಾ ಕ್ರಮ ಸಂಖ್ಯೆ

University Seat Number : 2GI02EC402

ವಿಷಯ

Subject : *Electronics & Communication Engineering*

ಶ್ರೇಣಿ

Class : *Second Class*

JD 22534  
04

ವಿಶ್ವವಿದ್ಯಾಲಯದ ಮುದ್ರೆಯೊಂದಿಗೆ ಕೊಡಲ್ಪಟ್ಟಿದೆ  
Given under the seal of the University

ಬೆಳಗಾವಿ

Belgaum

ದಿನಾಂಕ

Date : FEB 25, 2007

ಕುಲಪತಿ

VICE CHANCELLOR



**ವಿಶ್ವವಿದ್ಯಾನಿಲಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಂಗಳೂರು**  
**VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM**  
 KARNATAKA, INDIA



FR 029271

M.Tech. Fourth Semester ( Digital Electronics & Communication Systems )

Name of the Student : TANUJA I. HUKKERI  
 Father's / Mother's Name : LAVAKUMAR S HUKKERI  
 Name of the College : MARATHA MANDAL'S ENGINEERING COLLEGE, BELGAUM

Seat No. : 2MM12LDS23

Sl. No.	Code	Subject Title	Examination Marks			Internal Assessment Marks			Total Marks			Month & Year of Passing
			Max	Min	Obtained	Max	Min	Obtained	Max	Min	Obtained	
1	12LDS41	Evaluation of Project Phase - II	-	-	-	50	25	47	50	25	47	Pass
2	12LDS42	Evaluation of Project Work - III	-	-	-	50	25	49	50	25	49	Pass
3	12LDS43	Project Work Evaluation & Viva Voce GRAY CODE IMPLEMENTATION FOR AREA QUERY PROCESSING IN WIRELESS SENSOR NETWORK	200	100	186	-	-	-	200	100	186	Pass
<b>GRAND TOTAL</b>									300	150	282	

\*First Attempt Total (in words) : TWO HUNDRED AND EIGHTY TWO ONLY

Result of the Semester : FIRST CLASS WITH DISTINCTION

Results of I to IV Semesters :

Semesters	I	II	III	IV	I TO IV
Marks at First Attempt	542	566	375	282	1765
Total Marks (Max.)	800	800	500	300	2400
Result	FC	FCD	FCD	FCD	FCD



First Attempt Total ( I to IV Sem) in words : ONE THOUSAND SEVEN HUNDRED AND SIXTY FIVE ONLY

Result of the Degree : FIRST CLASS WITH DISTINCTION

Registrar (Evaluation)

NOV 22, 2014

ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ  
VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM  
KARNATAKA, INDIA



certifies that

**TANUJA L. HUKKERI**

**ಮಾಸ್ತರ್ ಆಫ್ ಟೆಕ್ನಾಲಜಿ**

ಇವರಿಗೆ ಈಗ ವಾಹಿ ಅರ್ಜಿ ಸಲ್ಲಿಸಿರುವುದರಿಂದ ಈಗಾಗಲೇ ಈಗಾಗಲೇ

ಮೇಲೆ ಕಾಣಿಸಿದ ವಿಷಯವನ್ನು ಈಗಾಗಲೇ

*has been duly advanced in the*

**Master of Technology**

*in recognition of the*

*for the*



ವಿಶ್ವವಿದ್ಯಾಲಯ ಸಂಖ್ಯೆ

University Seat Number

2MM12LDS23

ವಿಷಯ

Subject

*Digital Electronics & Communication Systems*

ಶಿಕ್ಷಣ

Class

*First Class with Distinction*

D 006058

ಇದು ಈಗಾಗಲೇ ಸಲ್ಲಿಸಿರುವುದರಿಂದ ಈಗಾಗಲೇ

ಮೇಲೆ ಕಾಣಿಸಿದ ವಿಷಯವನ್ನು ಈಗಾಗಲೇ

ಇವರಿಗೆ

ಇವರಿಗೆ

ಇವರಿಗೆ

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

HYDERABAD - 500 085, TELANGANA STATE, INDIA

TS 594789



H.T No. : 11M31D5817

College Code : M3 ( N C E T, LEMOOR )

*Mr. Wagh Mukund Baburao  
S/o. Wagh Babu Rao Laknath*

having fulfilled the academic requirements and passed the examination held during *December, 2013* in *First Class With Distinction* has this day been admitted by the Executive Council to the Degree of

*Master Of Technology  
Computer Science & Engineering*

Given under the Seal of the University

Date: 04-09-2014



DIRECTOR OF EVALUATION

REGISTRAR

VICE CHANCELLOR

CMM No  
20198002243



45200198158220

PC No  
30198002197





**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD**  
**HYDERABAD - 500 085, ANDHRA PRADESH, INDIA**

**CONSOLIDATED MARKS MEMO - CREDIT SHEET**

CMM. No. : **C 0362161**

**Master of Technology- COMPUTER SCIENCE & ENGINEERING**

Serial No. : 20198002243

Name of the College : **N C E T, LEMOOR**

Name : **WAGH MUKUND BABURAO**

Month & Year of Final Exam : **December, 2013**

Hall Ticket No. : **11M31D5817**

Year of Admission : **2011-2012**

Class Awarded : **FIRST CLASS WITH DISTINCTION**

S.No	SUBJECT TITLE	INT MARKS	EYE MARKS	Total Marks	Pass(F)/ Fail(F)
		40	60	100	
		40	60	100	

**I SEMESTER**

1	ADVANCED PROBLEM SOLVING				
2	COMPUTER SYSTEM DESIGN	35	38	73	P
3	EMBEDDED SYSTEMS	32	31	63	P
4	JAVA AND WEB TECHNOLOGIES	38	42	80	P
5	OBJECT ORIENTED MODELING	29	29	58	P
6	ADVANCED COMPILER DESIGN	36	39	75	P
7	JAVA AND WEB TECHNOLOGIES LAB	38	33	71	P
8	SEMINAR	30	43	73	P
		36	-	36	P

**II SEMESTER**

1	DISTRIBUTED COMPUTING				
2	DISTRIBUTED DATABASES	32	30	62	P
3	ADVANCED COMPUTER ARCHITECTURE	32	29	61	P
4	ADVANCED COMPUTER NETWORKS	36	30	66	P
5	INFORMATION RETRIEVAL SYSTEMS	32	33	65	P
6	INFORMATION SECURITY	36	31	67	P
7	DATABASES AND COMPILER LAB	36	31	67	P
8	SEMINAR - II	39	50	89	P
		47	-	47	P

**III SEMESTER**

1	COMPREHENSIVE VIVA - VOCE		80	80	P
	(Comprehensive VIVA External= 100)				

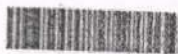
**IV SEMESTER**

1	PROJECT WORK*		A		P
	(* A - Excellent, B - Good, C - Satisfactory, D - Un Satisfactory)				

Aggregate Marks Secured: **1133 OUT OF 1600 (70.81%)**

Date of Issue: **January 16, 2014**

(see overleaf for Points concerned to award of class)



(\*Courses registered but not counted for calculation of aggregate)

*[Signature]*  
**CONTROLLER OF EXAMINATIONS**

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD  
HYDERABAD - 500 085, ANDHRA PRADESH, INDIA

College: M3 - N C E T, LEMOOR

30198002197

HT No: 11M31D5817

SI. No. PC 00467294



PROVISIONAL CERTIFICATE

20198002243

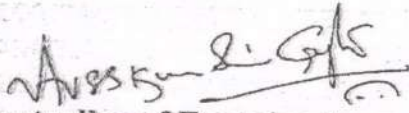
This is to certify that Mr. WAGH MUKUND BABURAO

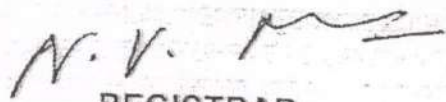
son of Mr. WAGH BABU RAO EAKNATH

passed M. Tech. (COMPUTER SCIENCE & ENGINEERING) degree  
examination of this University, held in December, 2013 and that he was  
placed in FIRST CLASS WITH DISTINCTION.

He has satisfied all the requirements for the award of the degree.

Hyderabad - A.P.

  
Controller of Examinations

  
REGISTRAR

Date: January 16, 2014



# JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

HYDERABAD - 500 085, TELANGANA STATE, INDIA

TS 688510



H. T No. : 12M51D5819

College Code : M5 ( R R S C E T, PATANCHERU )

COPY COPY COPY

*Mr. Kothawale Ganesh Sambhaji*  
*S/o. Sambhaji Gundaji Kothawale*

having fulfilled the academic requirements and passed the examination held during *November, 2014* in *First Class With Distinction* has this day been admitted by the Executive Council to the Degree of

*Master Of Technology*  
*Computer Science & Engineering*

Given under the Seal of the University

Date:17-09-2016



*CSE*

*H. K. S. P.*

*A. S. S.*

DIRECTOR OF EVALUATION

REGISTRAR

VICE CHANCELLOR

CMM No  
20210004101



460021052442

PC No  
30210004050